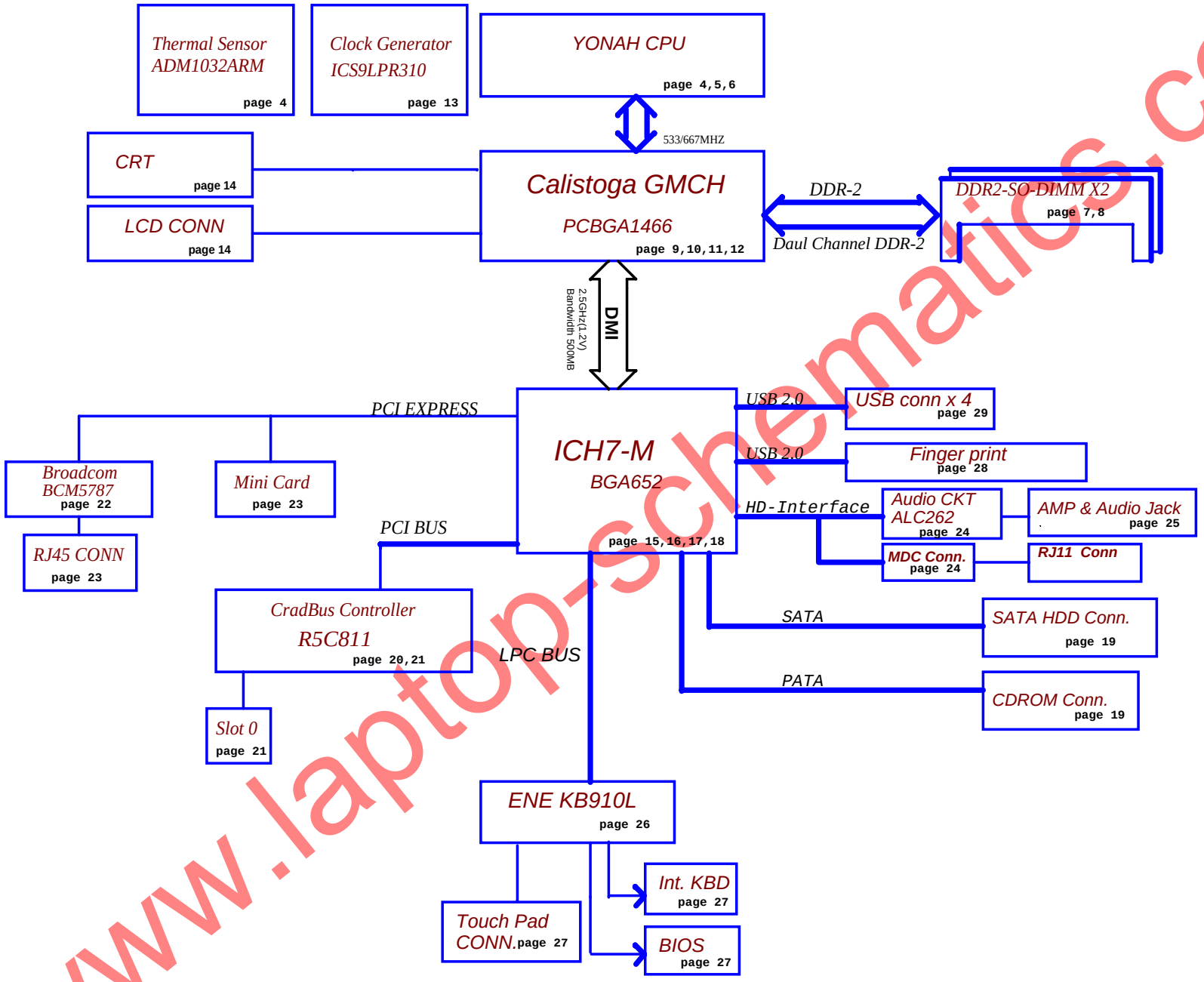
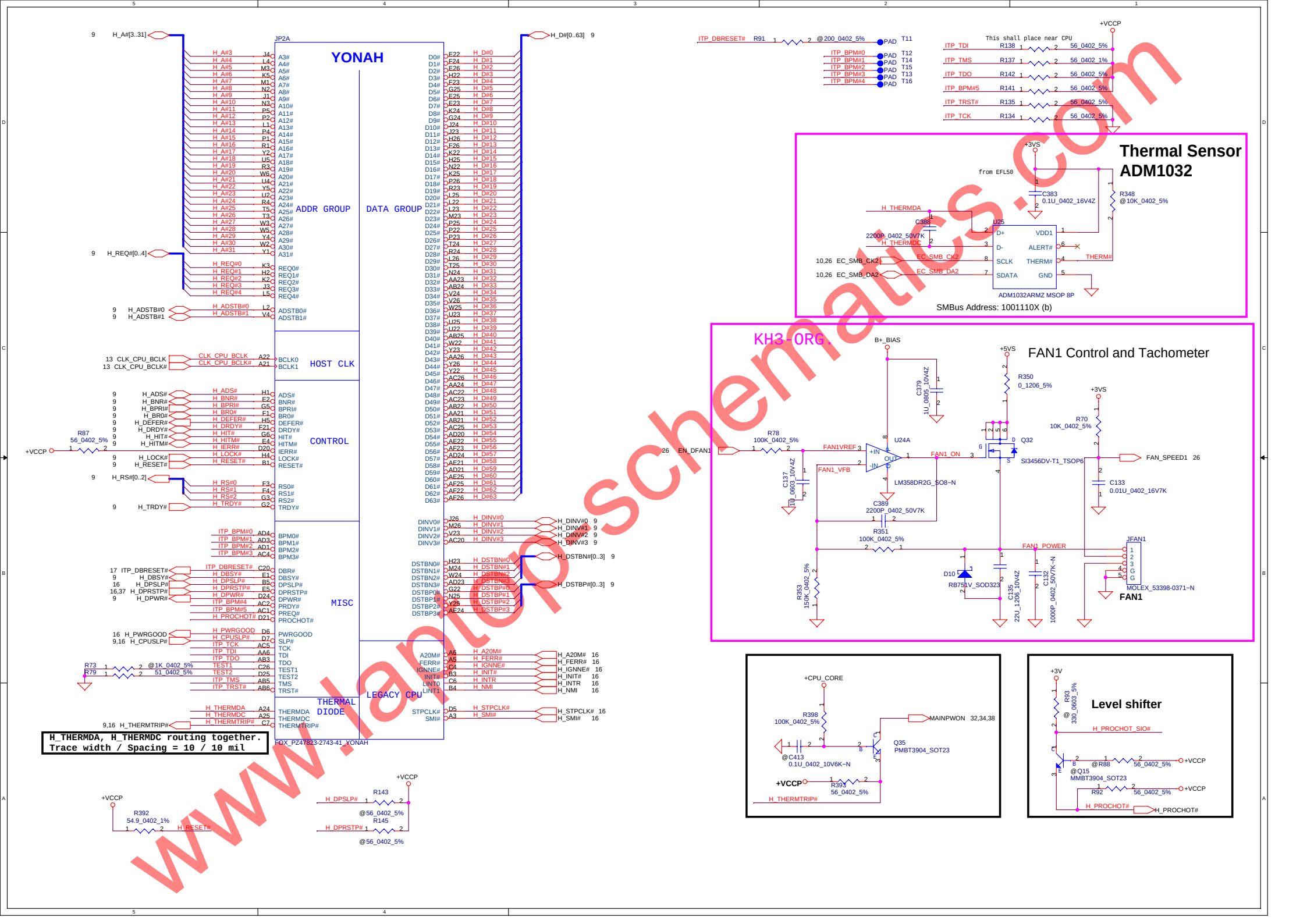
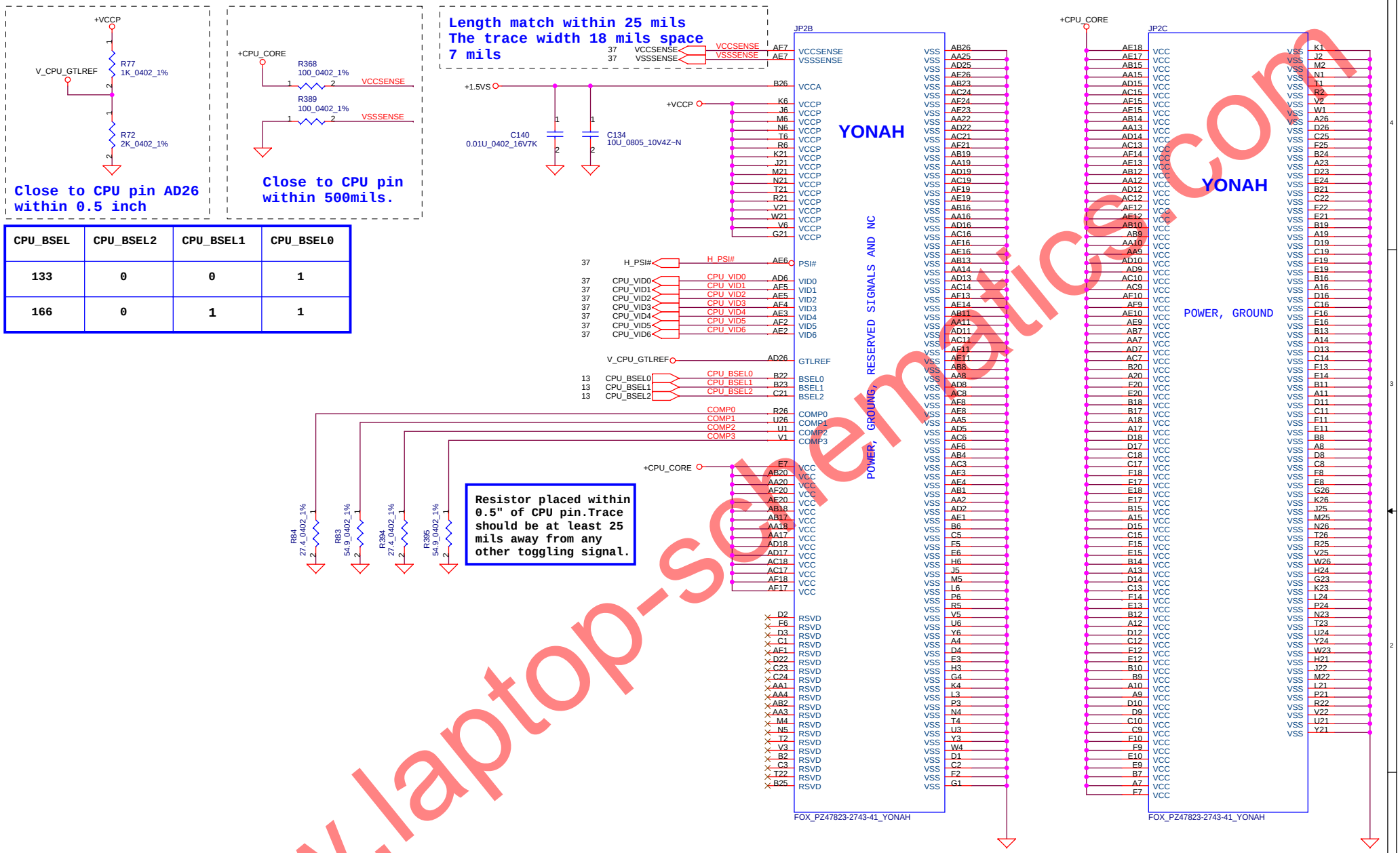
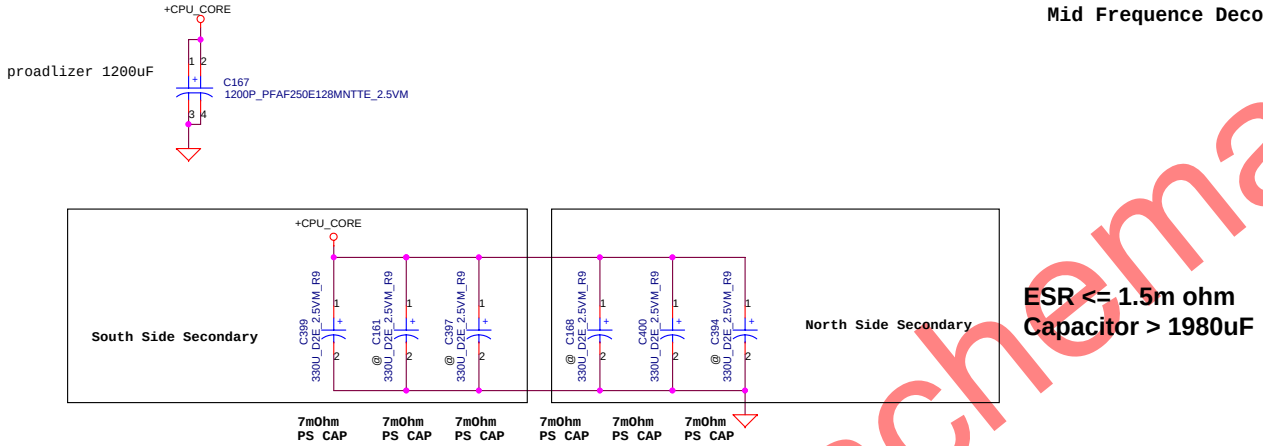
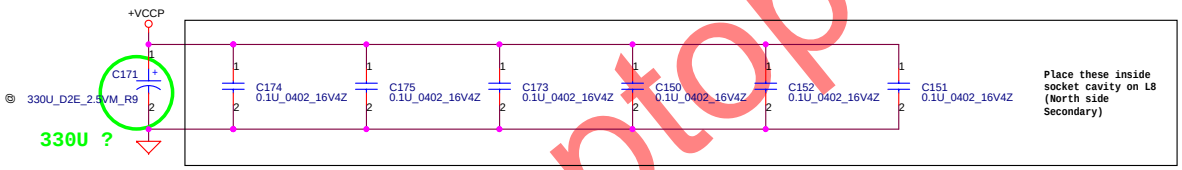


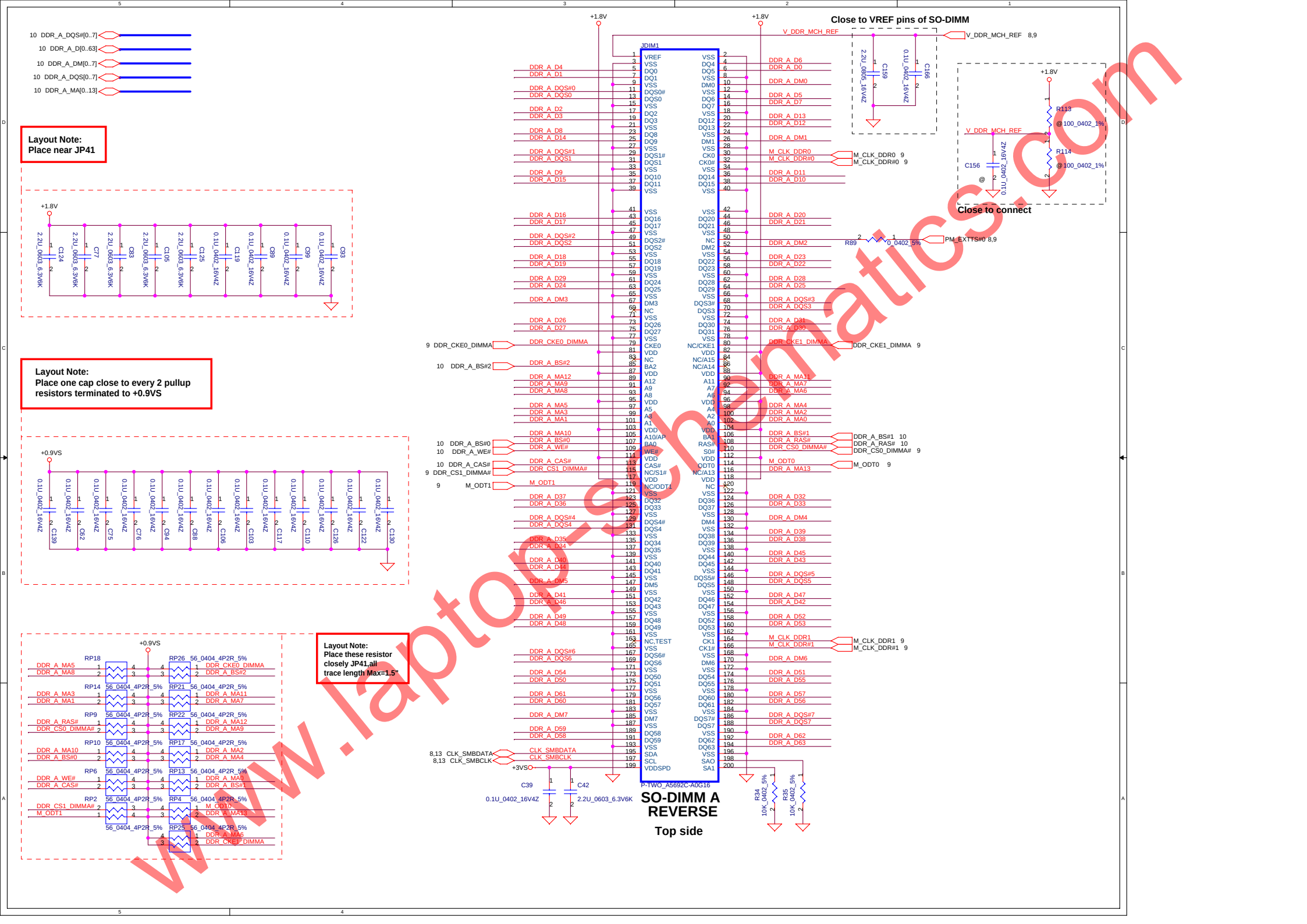


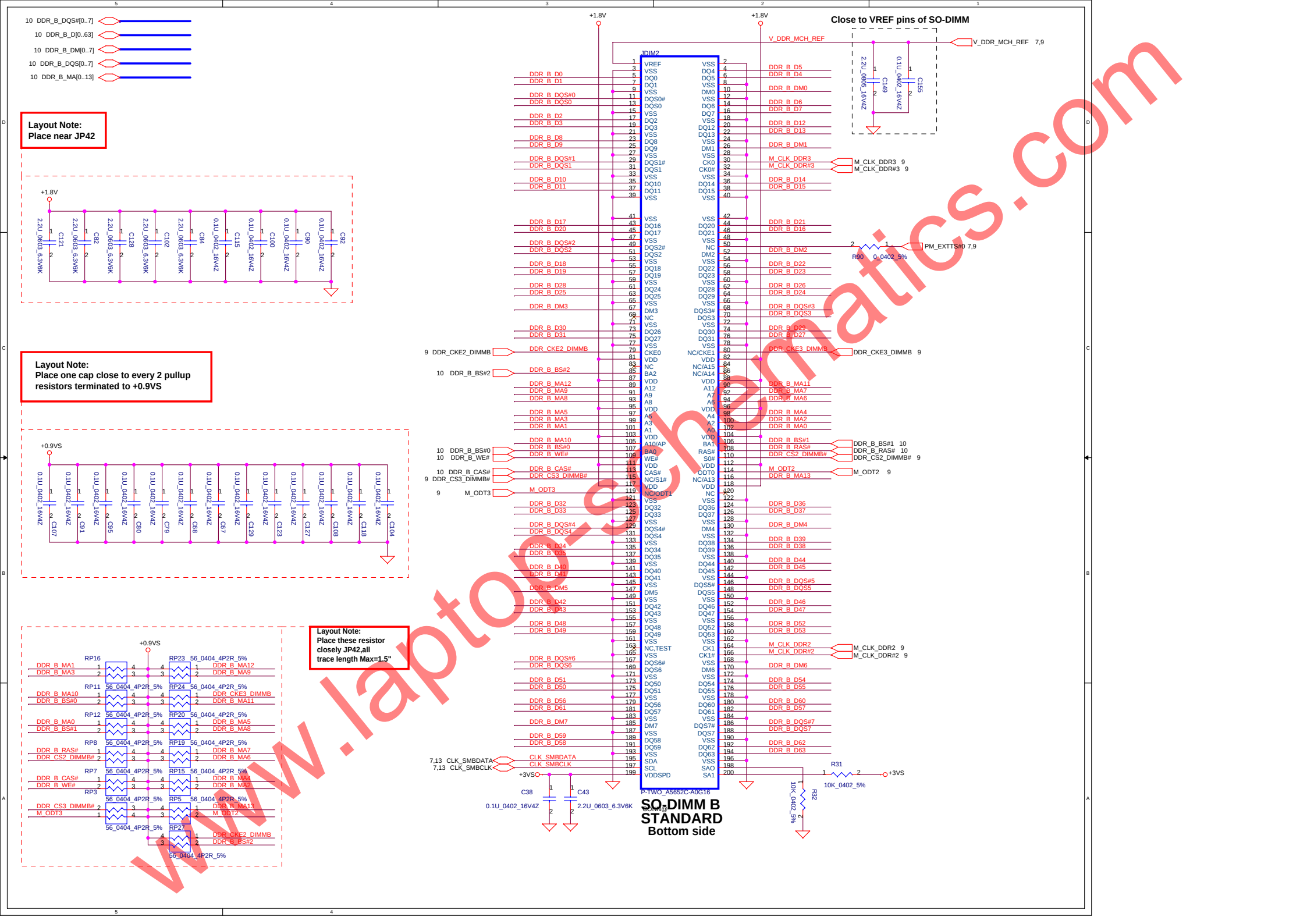


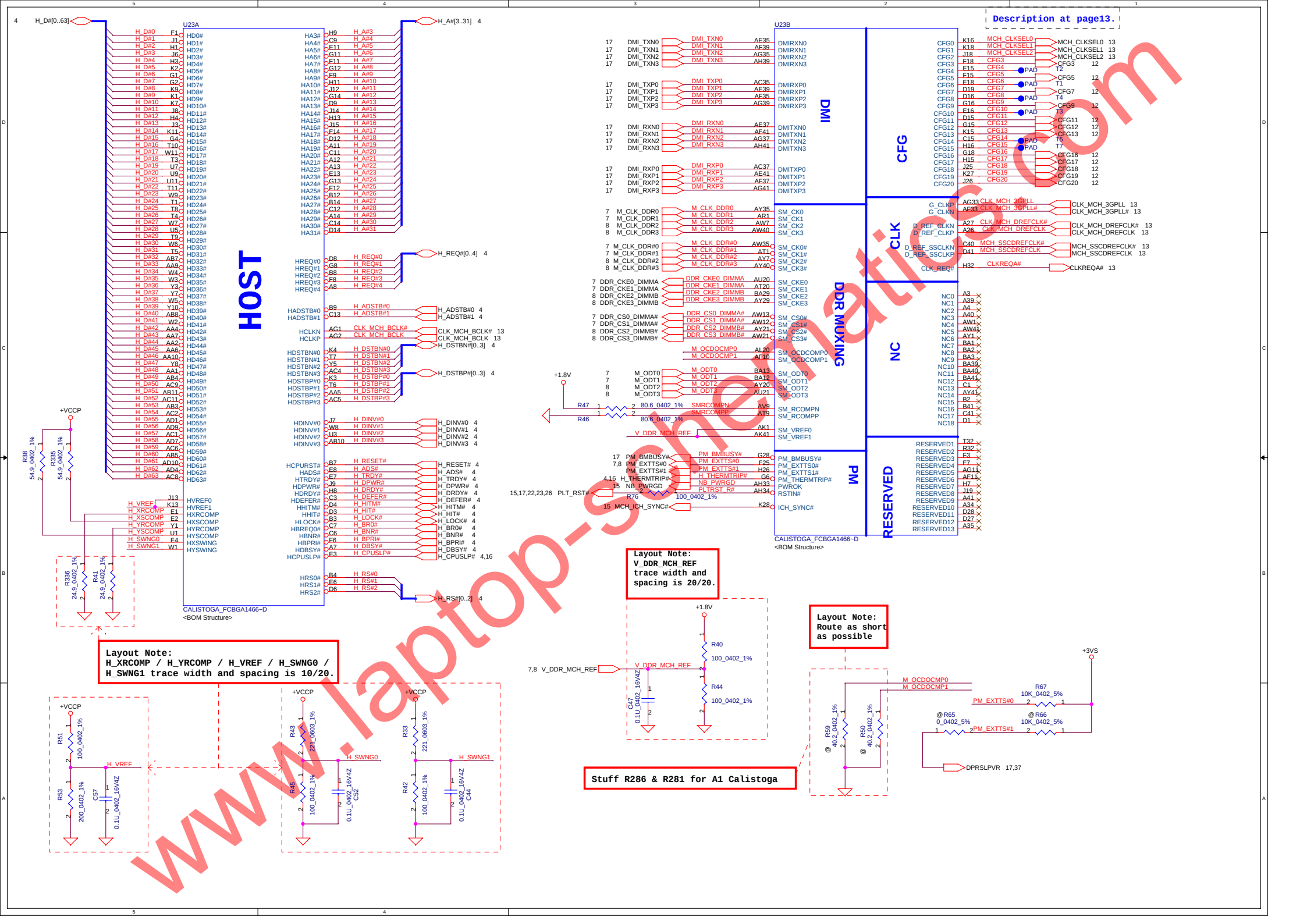
CPU_BSEL	CPU_BSEL2	CPU_BSEL1	CPU_BSEL0
133	0	0	1
166	0	1	1

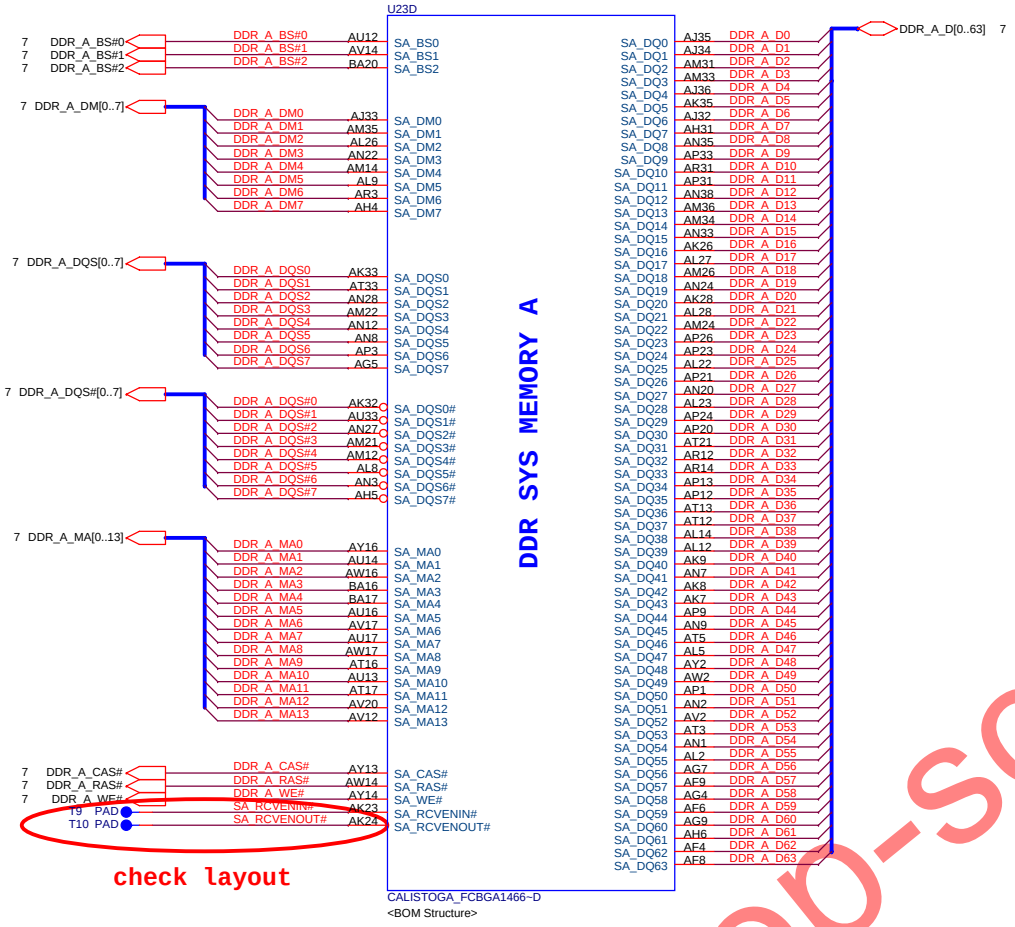


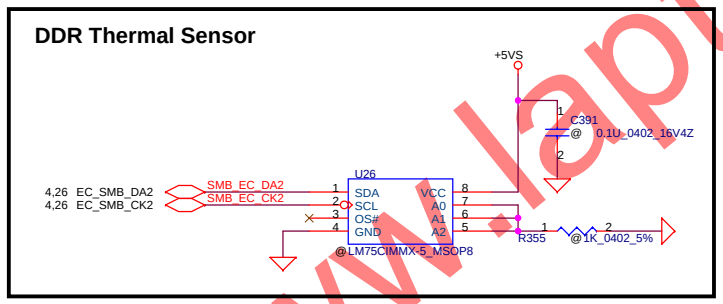


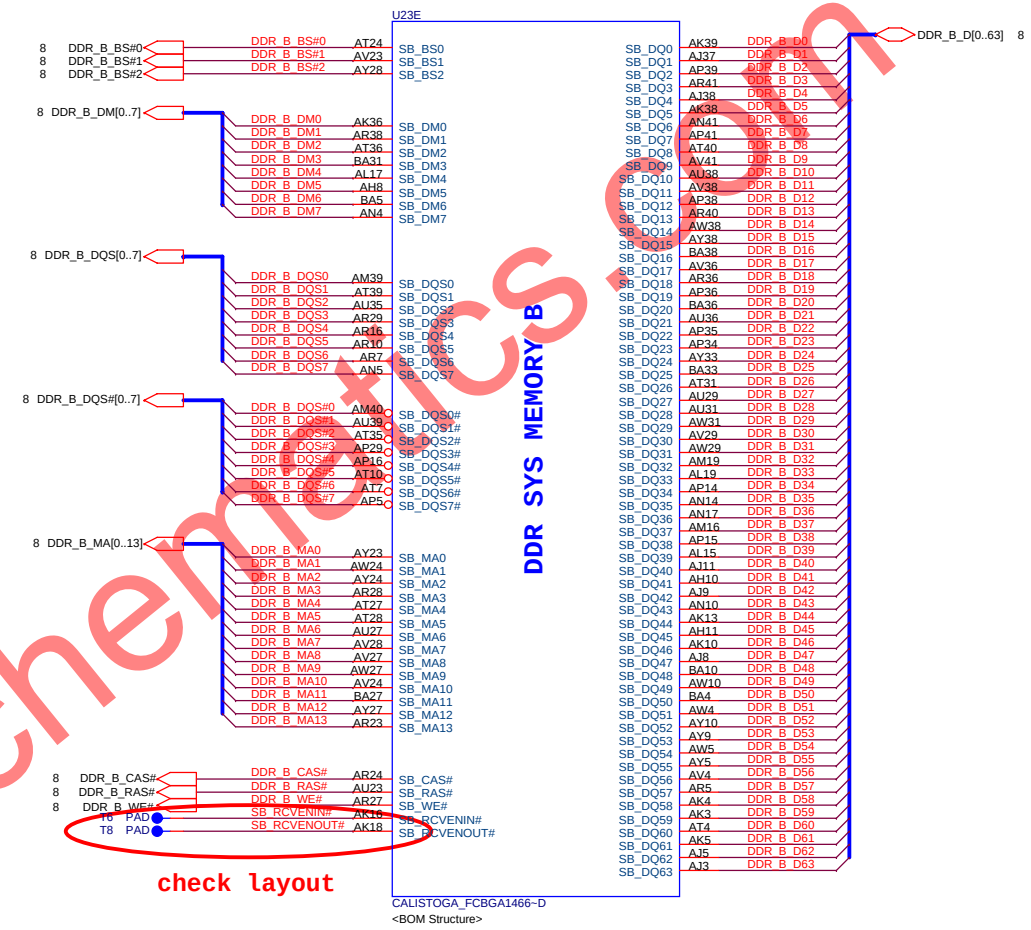


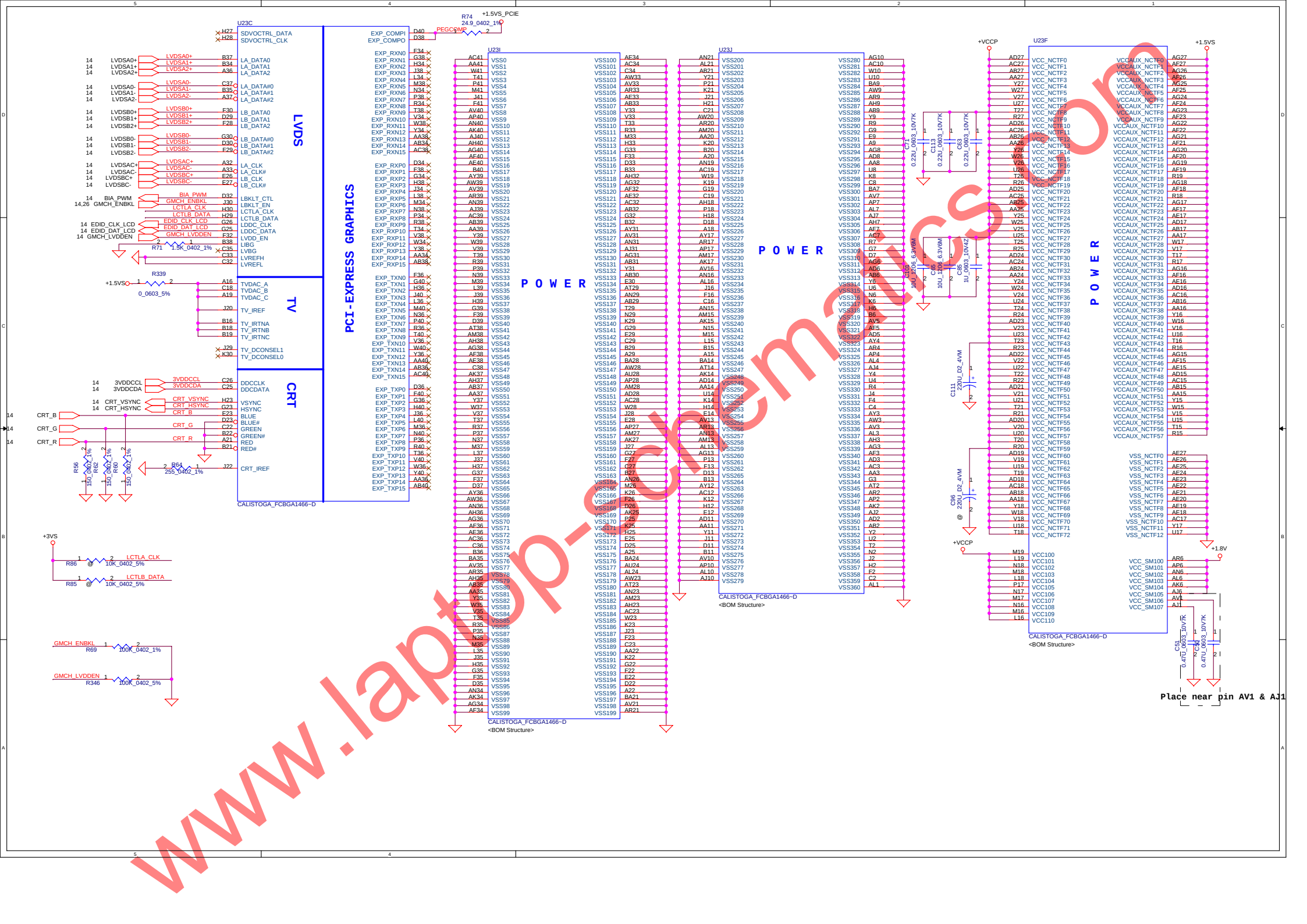


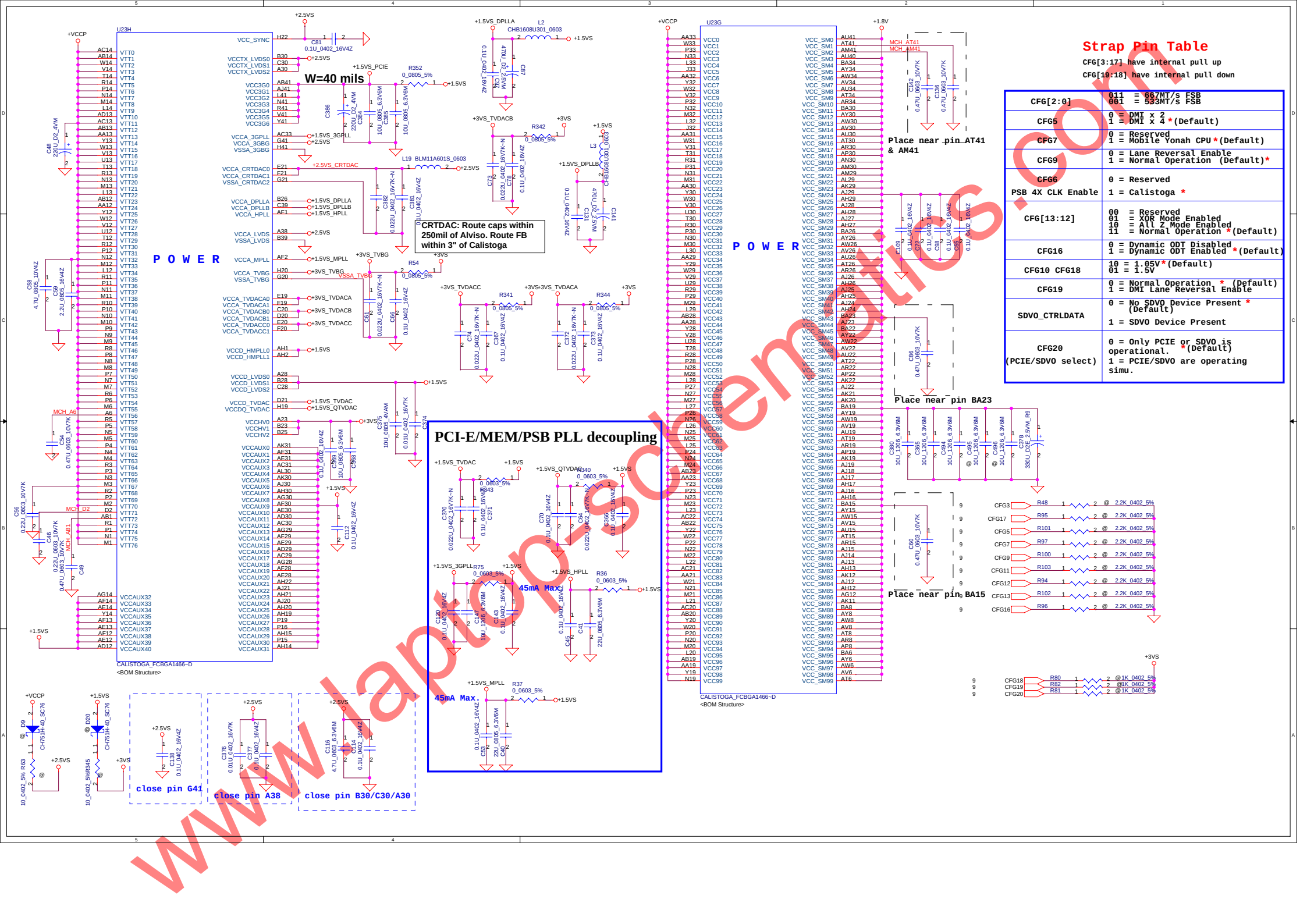


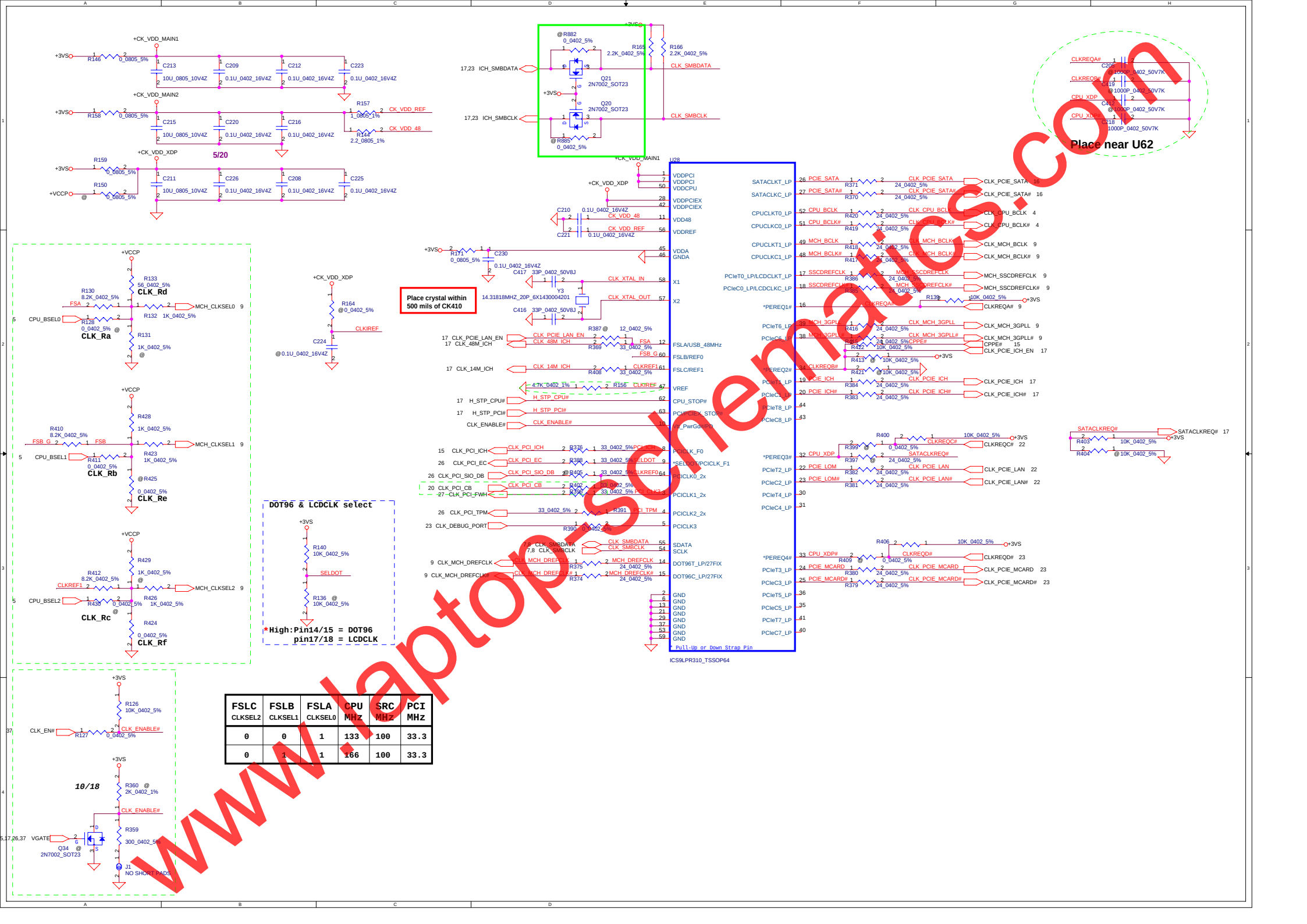




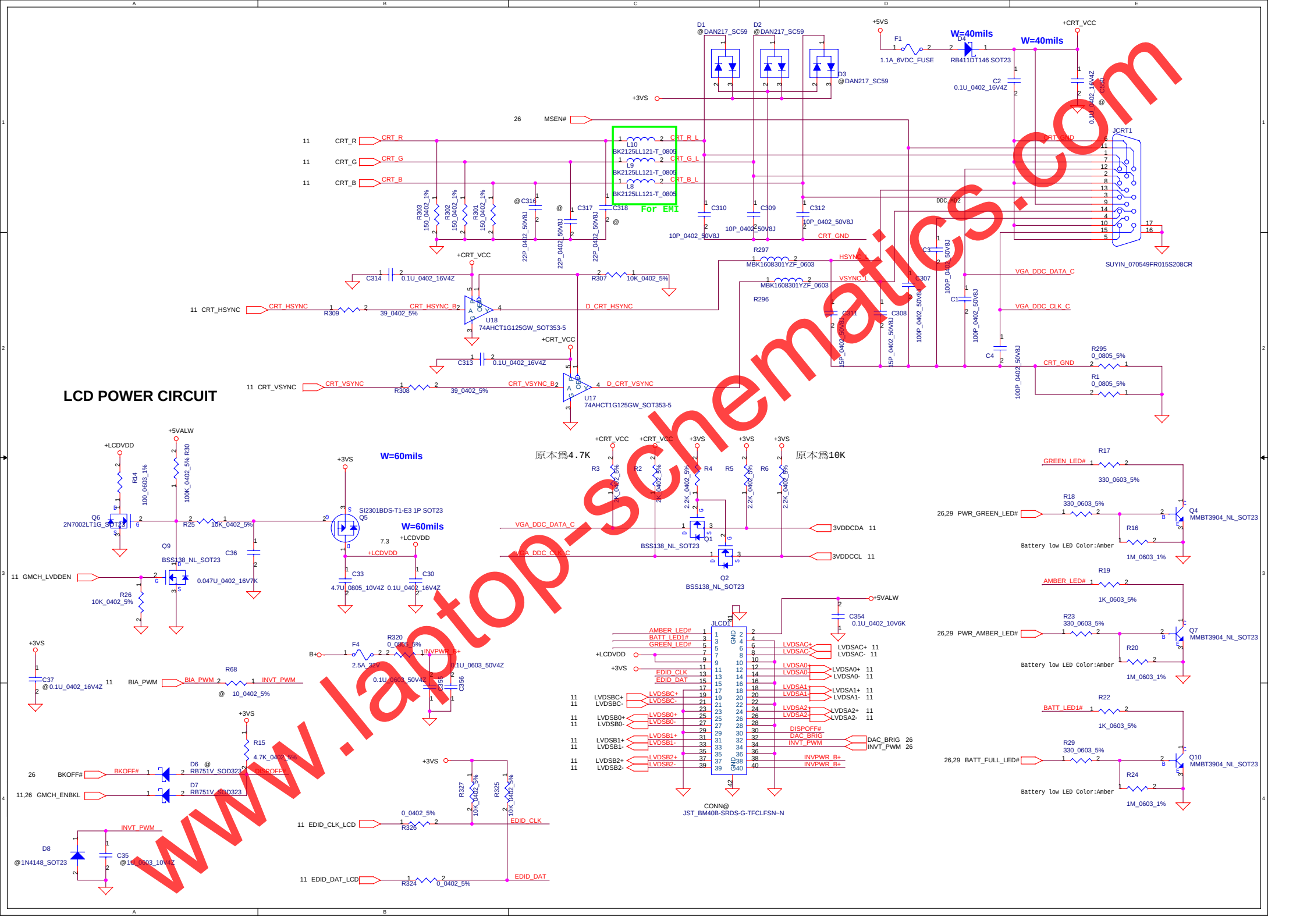


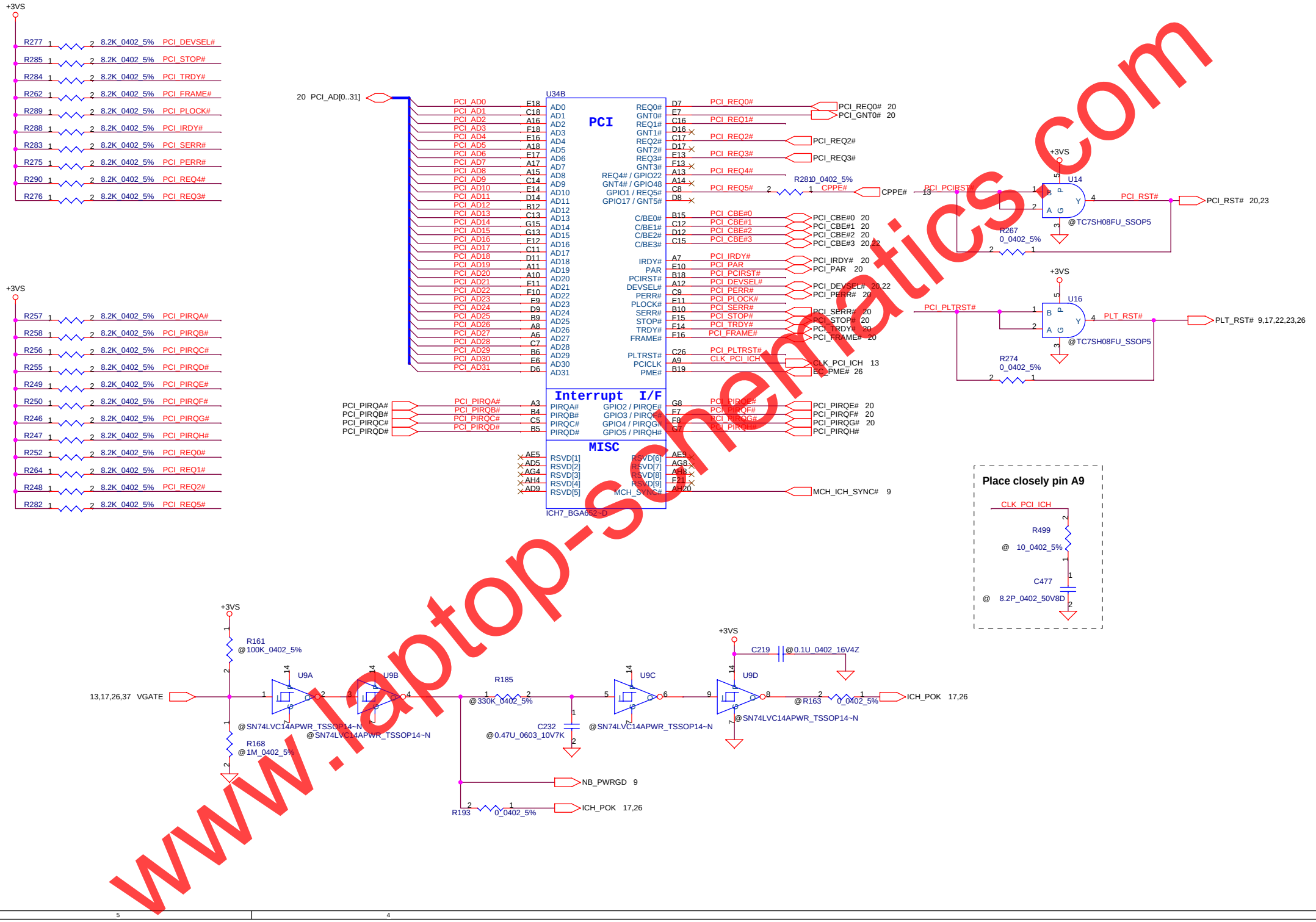


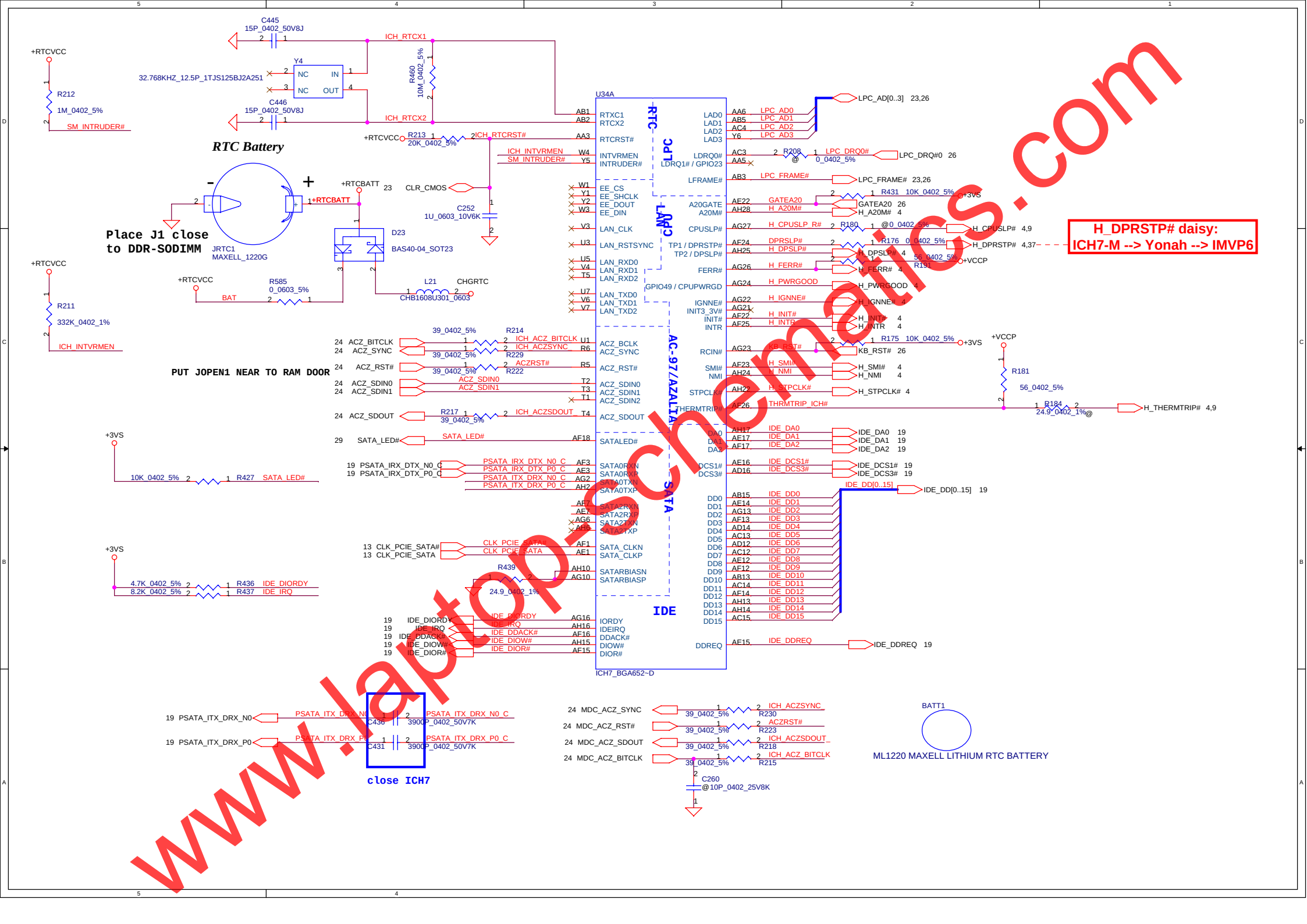


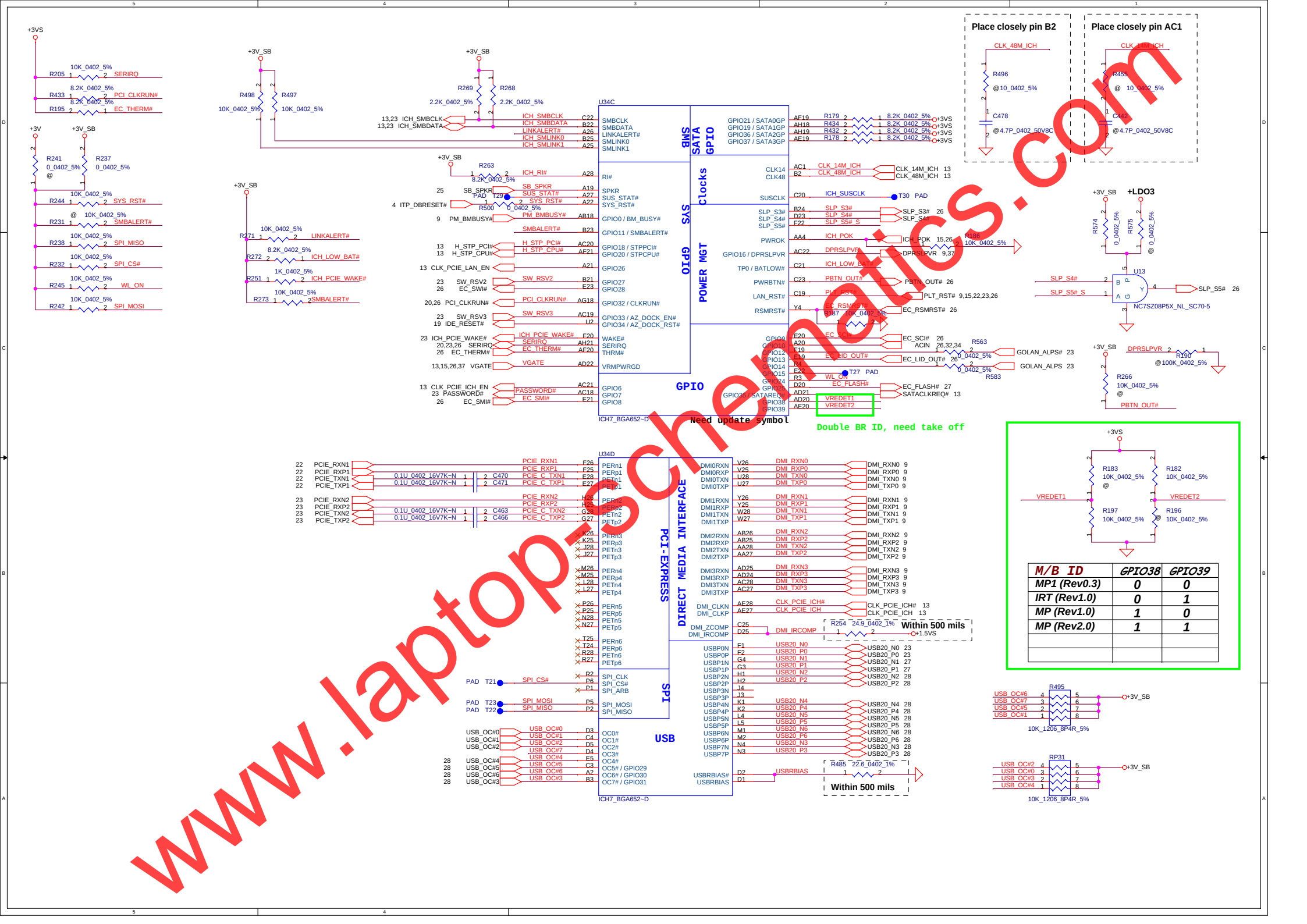


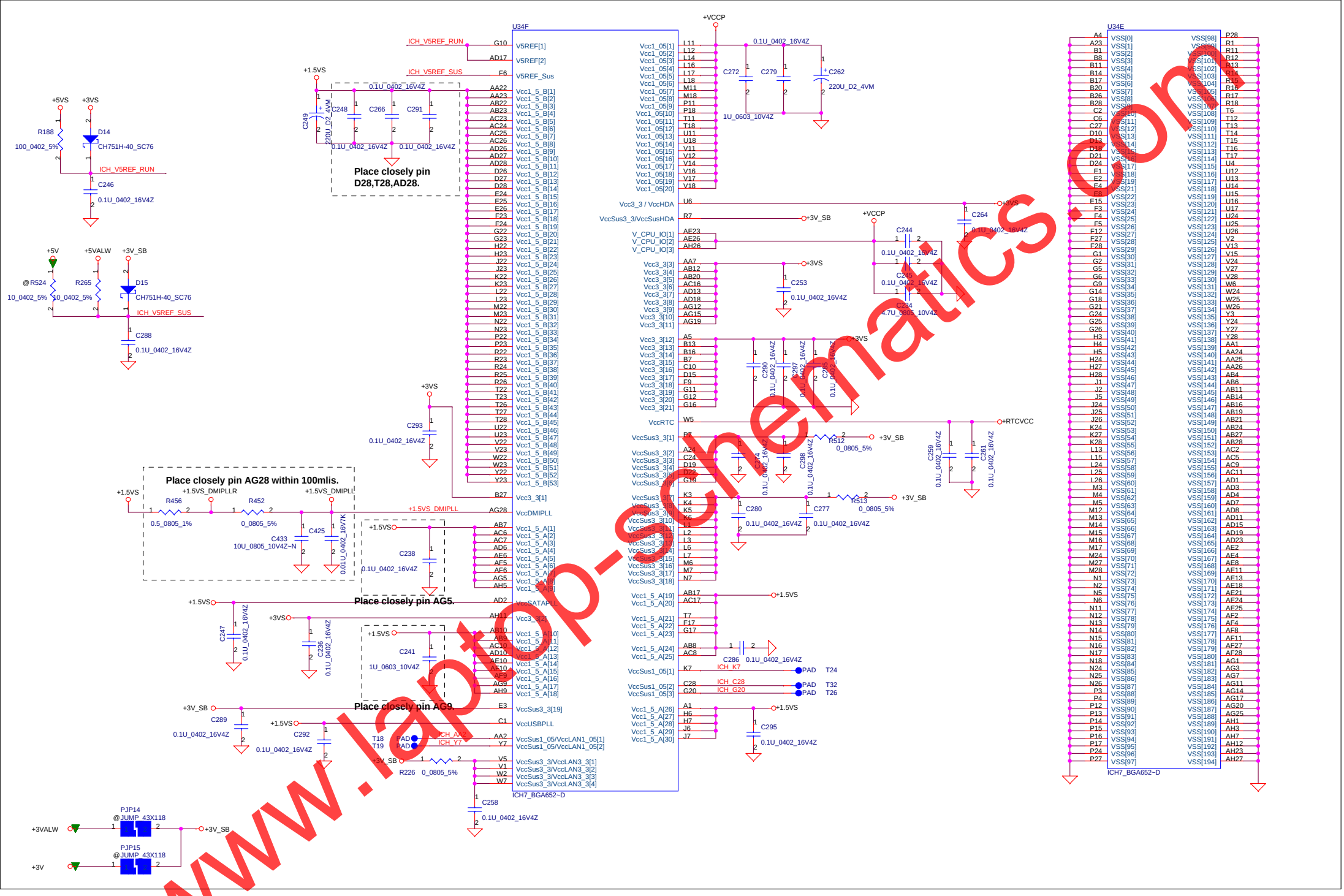
FSLC CLKSEL2	FSLB CLKSEL1	FSLA CLKSEL0	CPU MHz	SRC MHz	PCI MHz
0	0	1	133	100	33.3
0	1	1	166	100	33.3

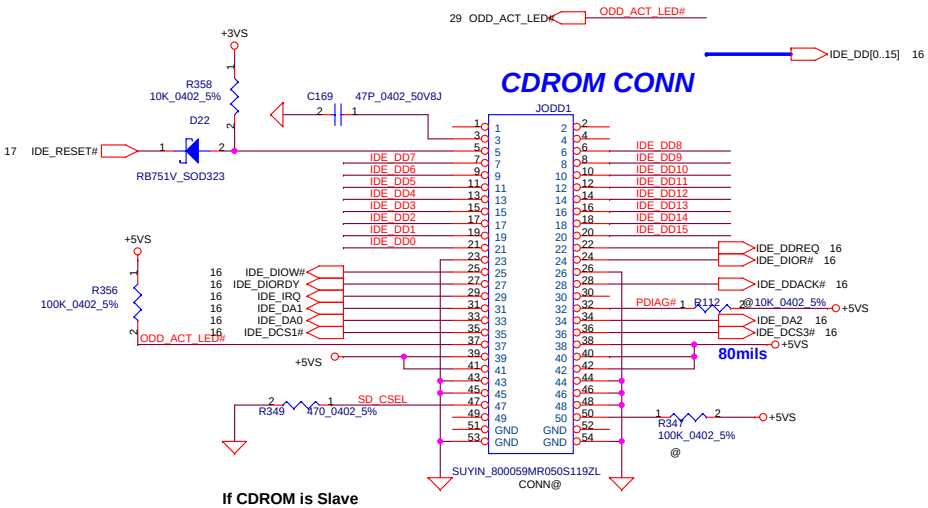


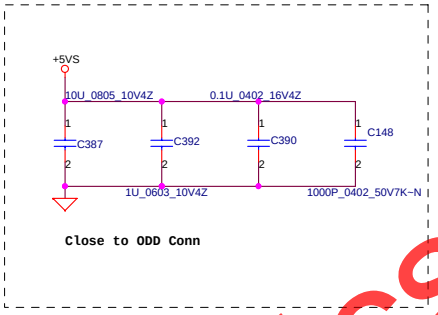


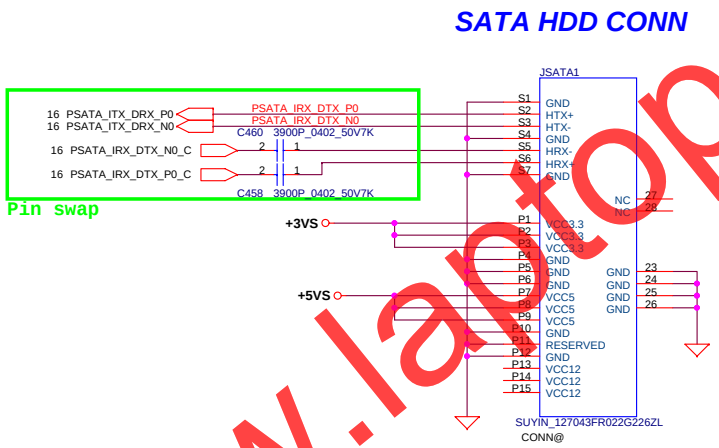

RTC Battery
Place J1 close to DDR-SODIMM
PUT JOPEN1 NEAR TO RAM DOOR
close ICH7
ML1220 MAXELL LITHIUM RTC BATTERY
H_DPRSTP# daisy:
ICH7-M --> Yonah --> IMVP6
U34A
ICH7_BGA652~D
RTC
LPC
LAN
AC-97/AZALIA
SATA
IDE
C445 15P_0402_50V8J
C446 15P_0402_50V8J
Y4 32.768KHZ_12.5P_1TJS125BJ2A251
R460 10M_0402_5%
R212 1M_0402_5%
R211 332K_0402_1%
R213 20K_0402_5%
C252 1U_0603_10V6K
D23 BAS40-04_SOT23
JRTC1 MAXELL_1220G
R585 0_0603_5%
L21 CHB1608U301_0603
R214 R229 R222 R217 R427 R436 R437 R439
R208 R431 R176 R180 R175 R181 R184 R191
C436 C431 C260
BATT1
www.laptopschematics.com

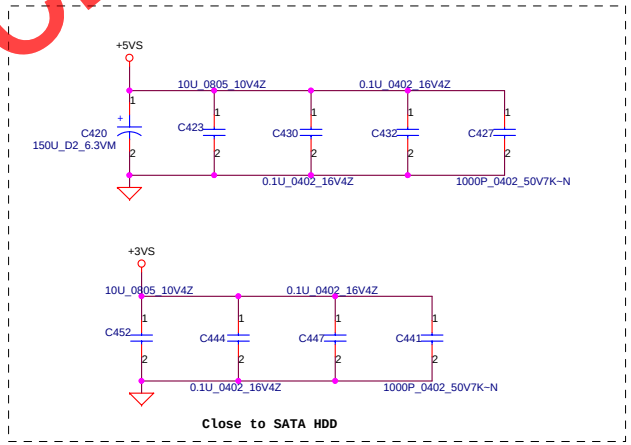

Place closely pin B2
Place closely pin AC1
Need update symbol
Double BR ID, need take off
Within 500 mils
Within 500 mils
GPIO
USB
SPI
PCI-EXPRESS
DIRECT MEDIA INTERFACE
SMB
SATA GPIO
Clocks
SYS
GPIO
POWER MGT
ICH7_BGA652-D
U34C
U34D
U13
+LDO3
M/B ID | GPIO38 | GPIO39
MP1 (Rev0.3) | 0 | 0
IRT (Rev1.0) | 0 | 1
MP (Rev1.0) | 1 | 0
MP (Rev2.0) | 1 | 1
www.laptop-schematics.com

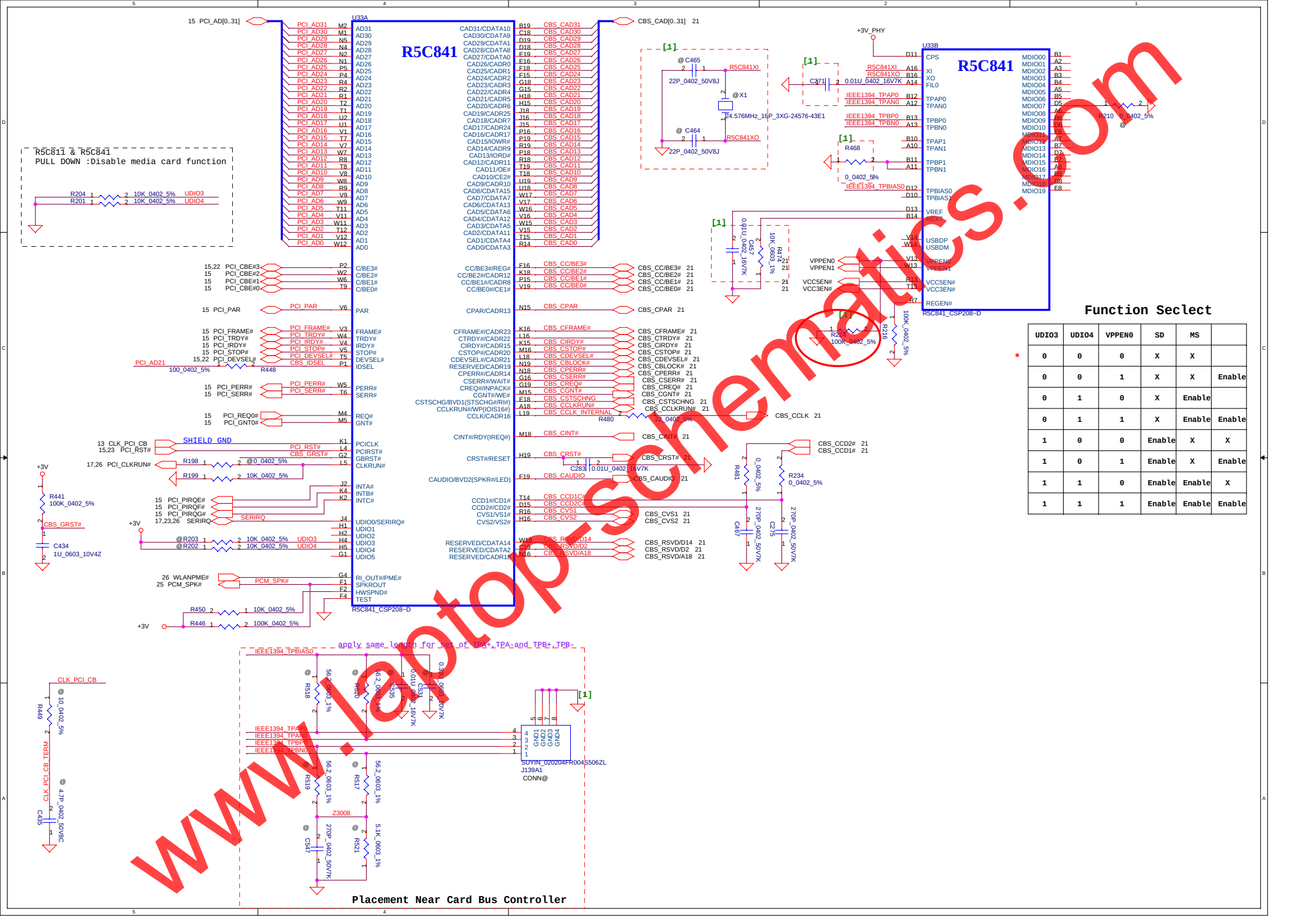


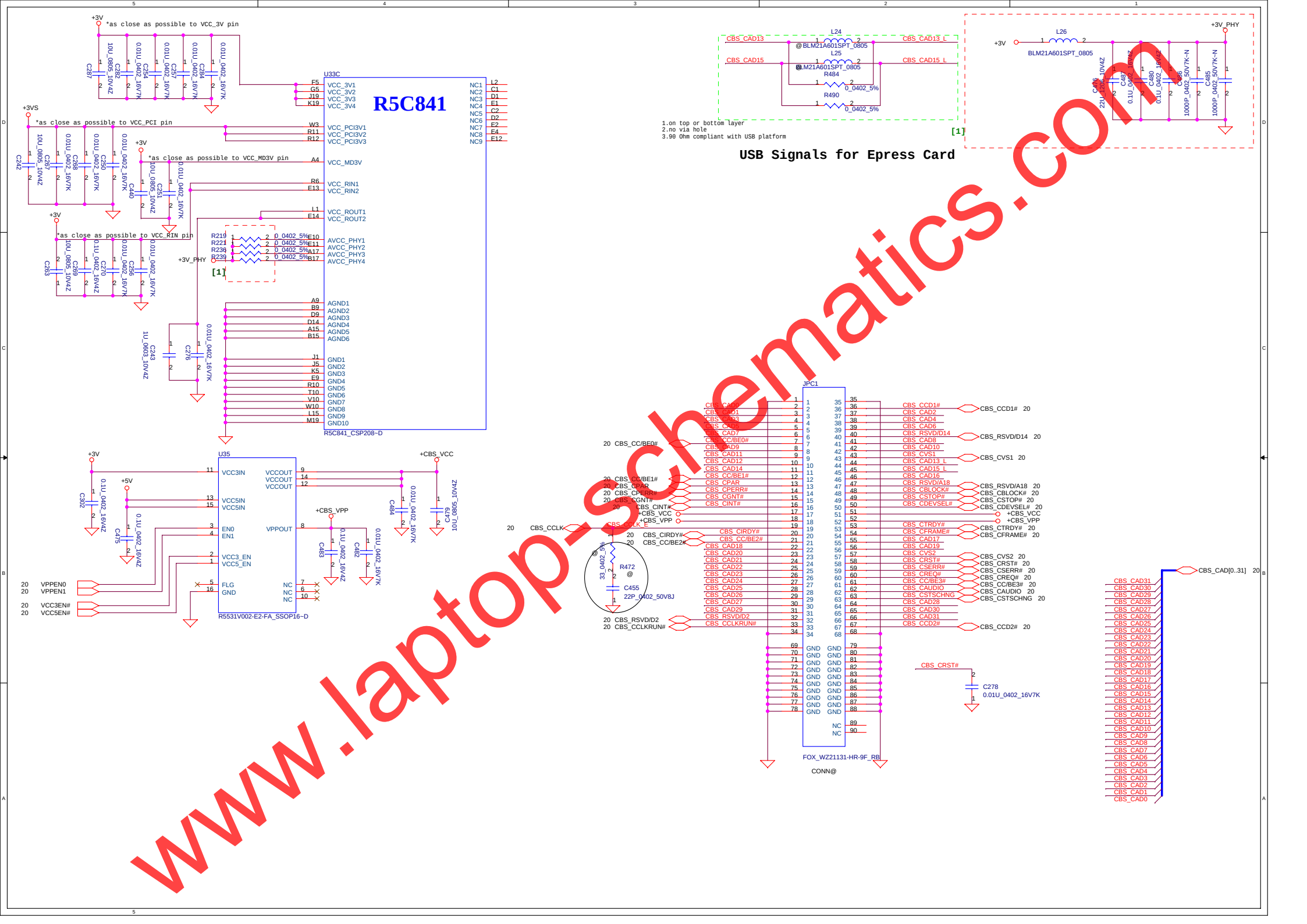


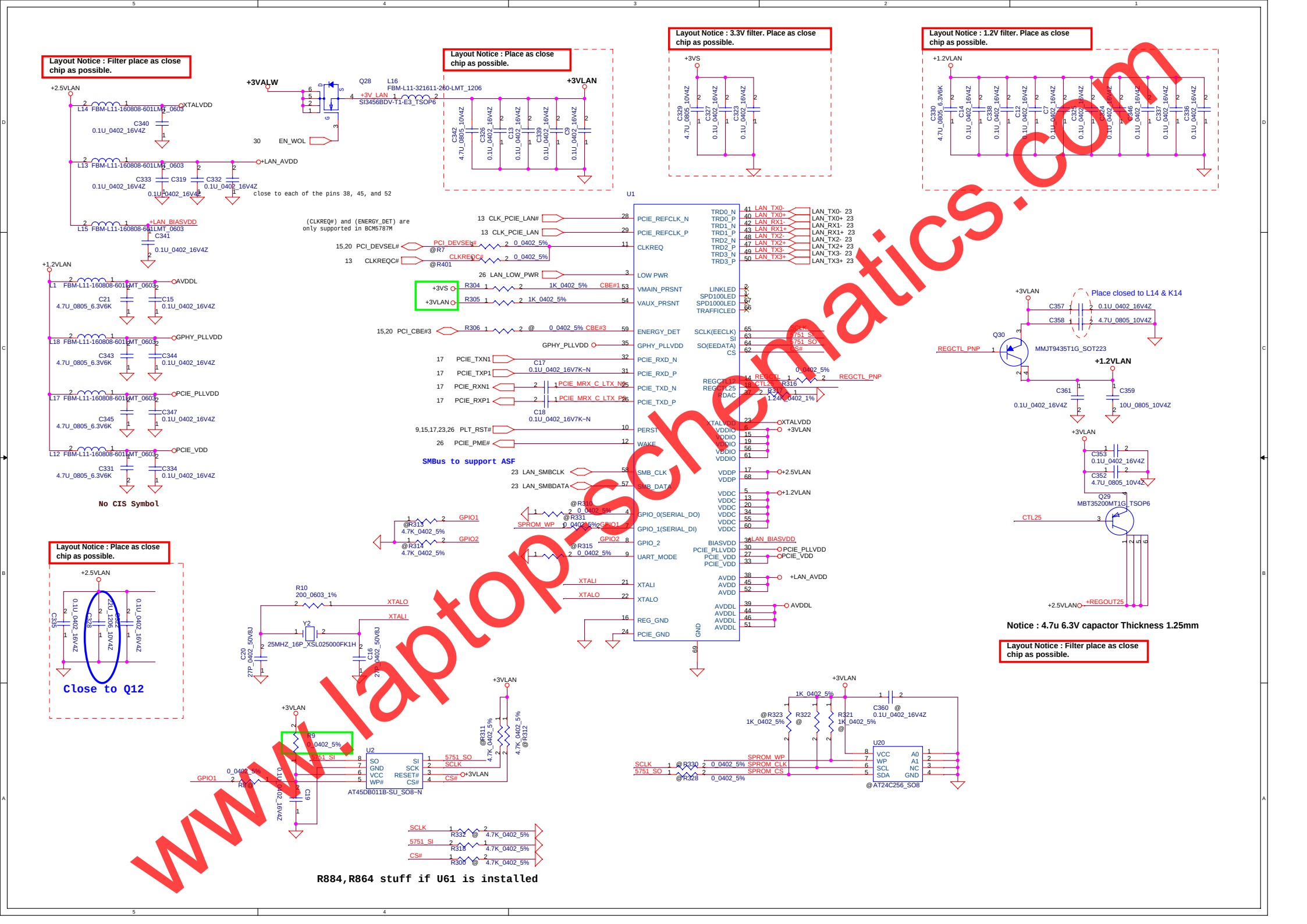


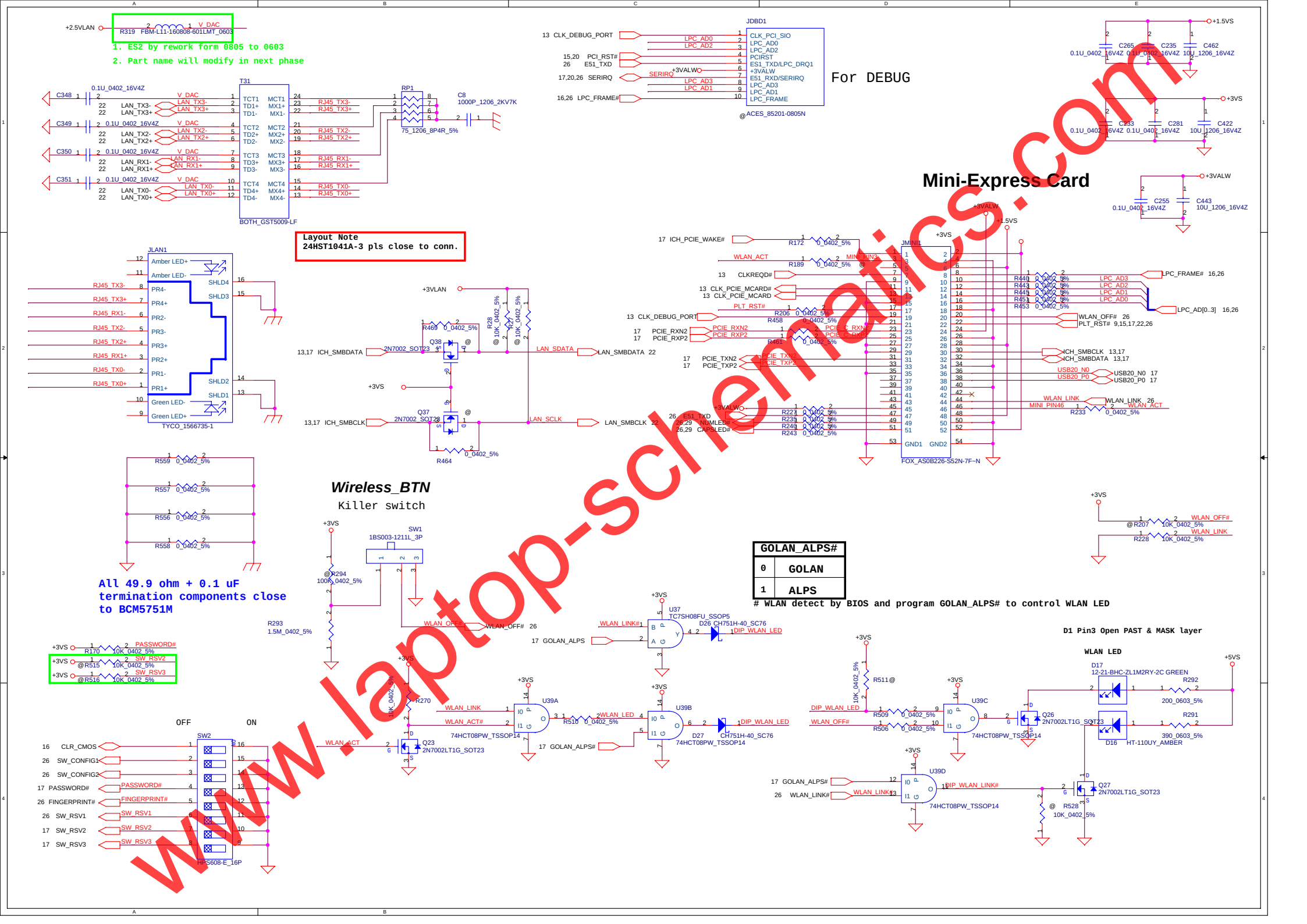


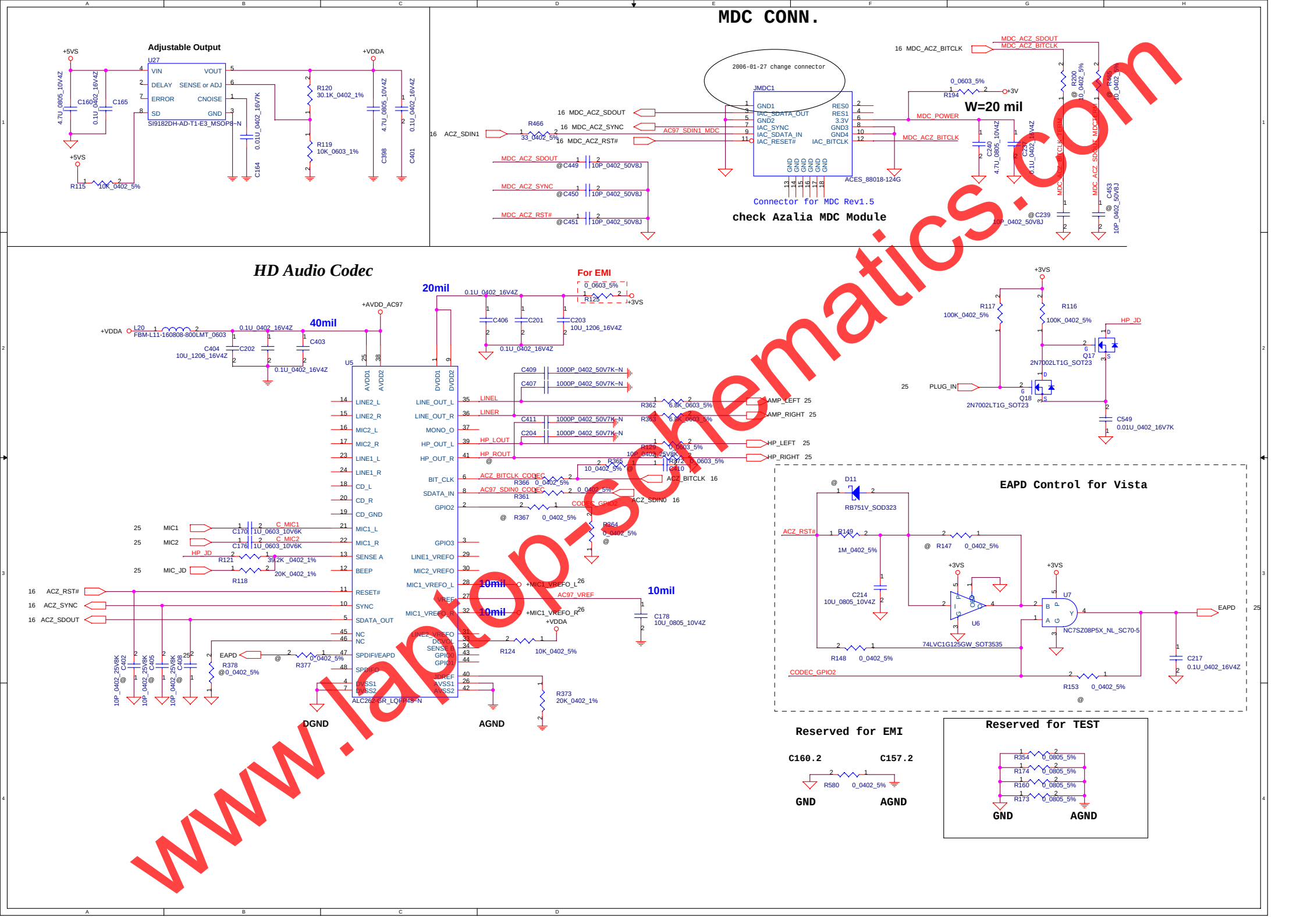


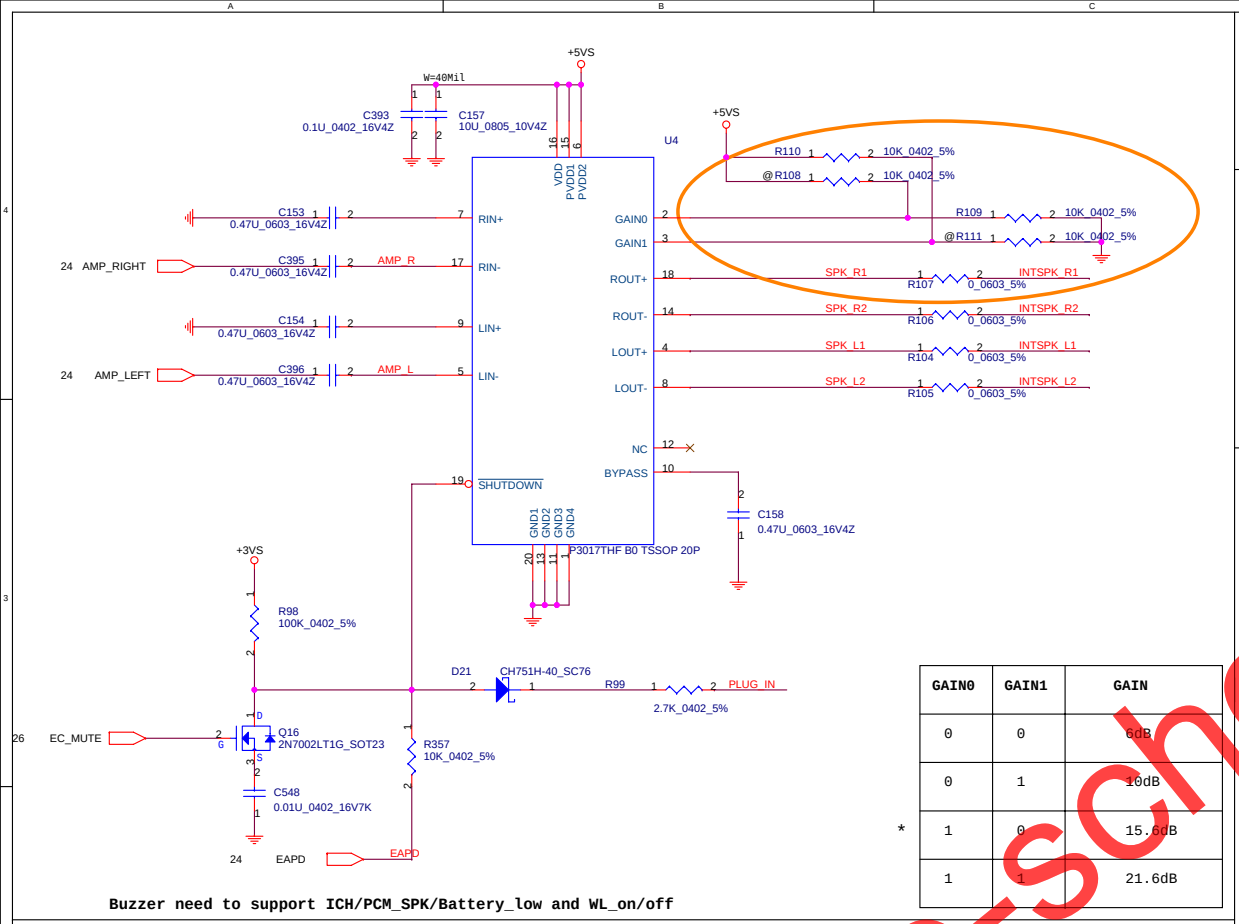

R5C841
R5C841
R5C841 & R5C841
PULL DOWN :Disable media card function
Function Seclect
UDIO3 UDIO2 VPPEN0 SD MS
0 0 0 X X
0 0 1 X X Enable
0 1 0 X Enable
0 1 1 X Enable Enable
1 0 0 Enable X X
1 0 1 Enable X Enable
1 1 0 Enable Enable X
1 1 1 Enable Enable Enable
Placement Near Card Bus Controller
apply same length for set of TPA+,TPA- and TPB+,TPB-
SHIELD GND
www.laptop-schematics.com





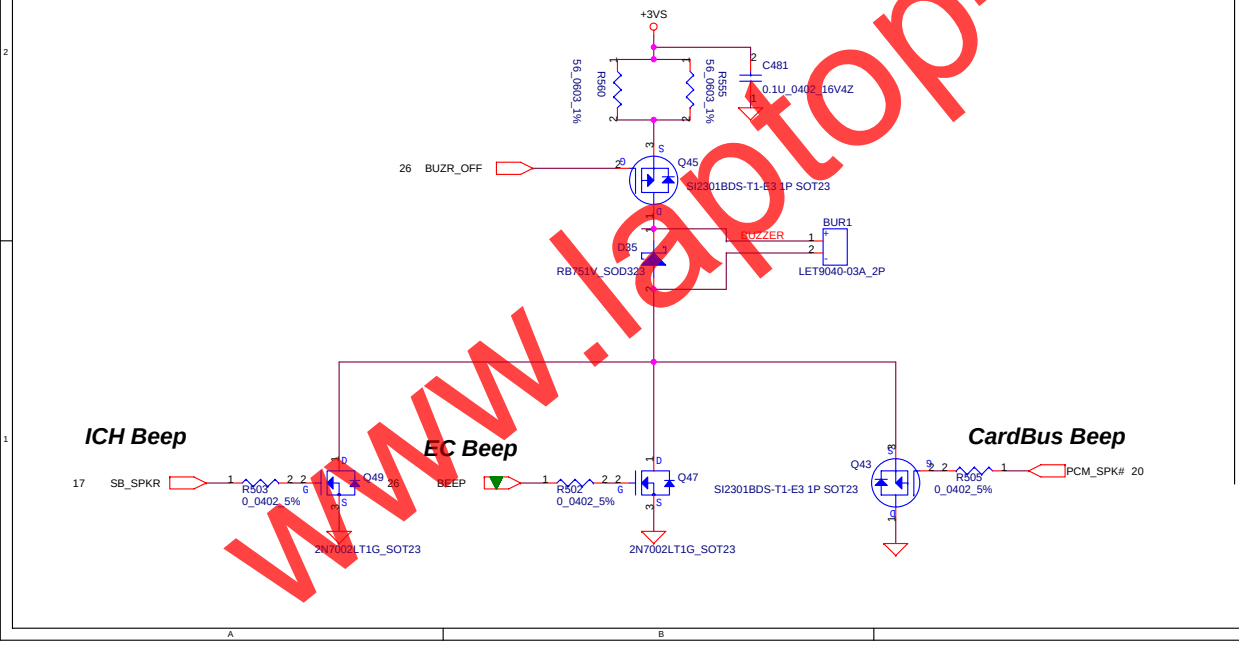

1. ES2 by rework form 0805 to 0603
2. Part name will modify in next phase
For DEBUG
Mini-Express Card
Layout Note
24HST1041A-3 pls close to conn.
Wireless_BTN
Killer switch
All 49.9 ohm + 0.1 uF termination components close to BCM5751M
GOLAN_ALPS#
0 GOLAN
1 ALPS
WLAN detect by BIOS and program GOLAN_ALPS# to control WLAN LED
D1 Pin3 Open PAST & MASK layer
WLAN LED
www.laptop-schematics.com


MDC CONN.
Adjustable Output
U27
SI9182DH-AD-T1-E3_MSOP8-N
2006-01-27 change connector
JMDC1
ACES_88018-124G
Connector for MDC Rev1.5
check Azalia MDC Module
W=20 mil
HD Audio Codec
ALC262_SR_LQFP48-N
For EMI
20mil
40mil
10mil
EAPD Control for Vista
Reserved for EMI
Reserved for TEST
DGND
AGND
GND
www.laptop-schematics.com

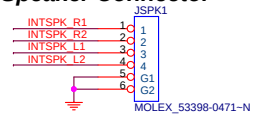


GAIN0	GAIN1	GAIN
0	0	6dB
0	1	10dB
* 1	0	15.6dB
1	1	21.6dB

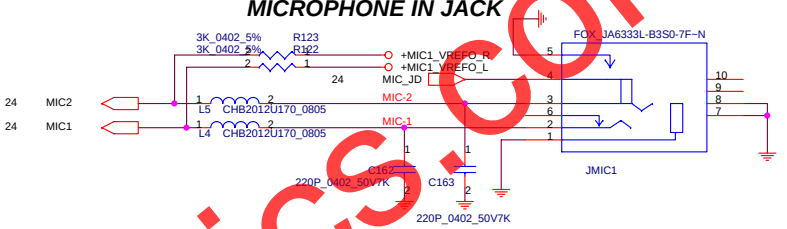
Buzzer need to support ICH/PCM_SPK/Battery_low and WL_on/off



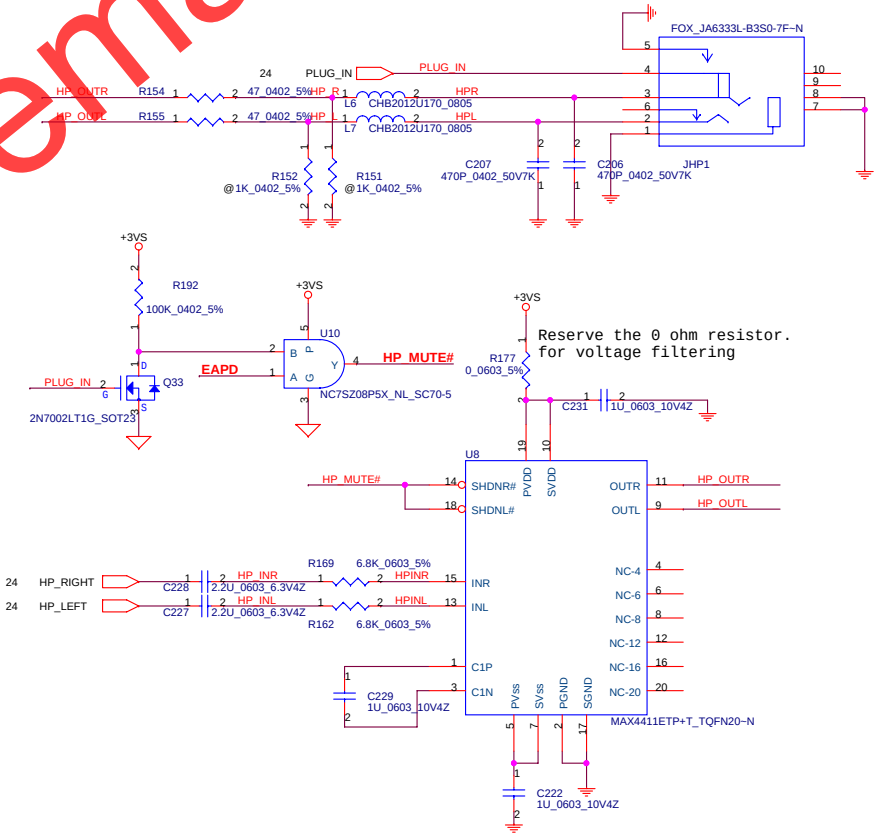
Speaker Connector

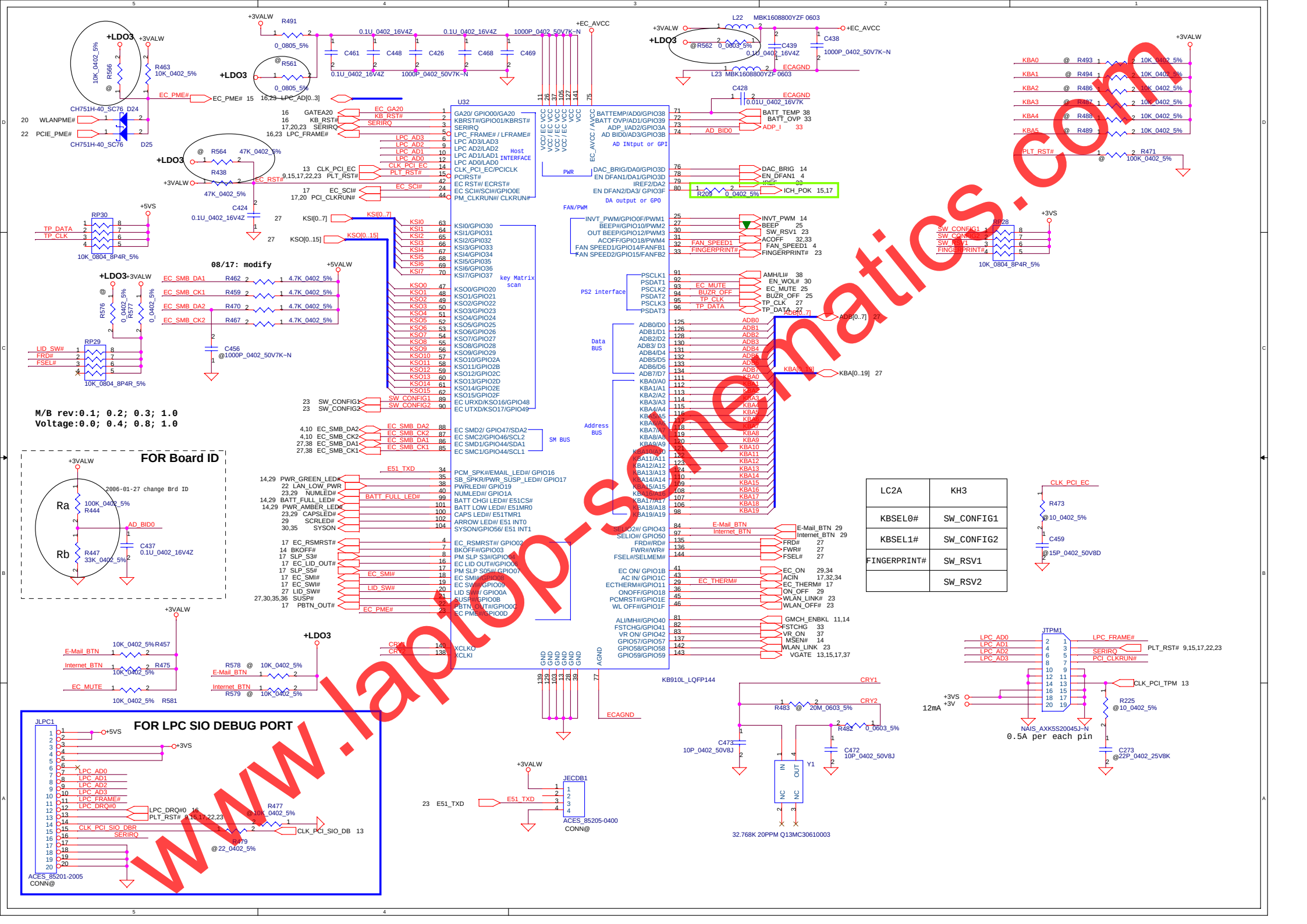


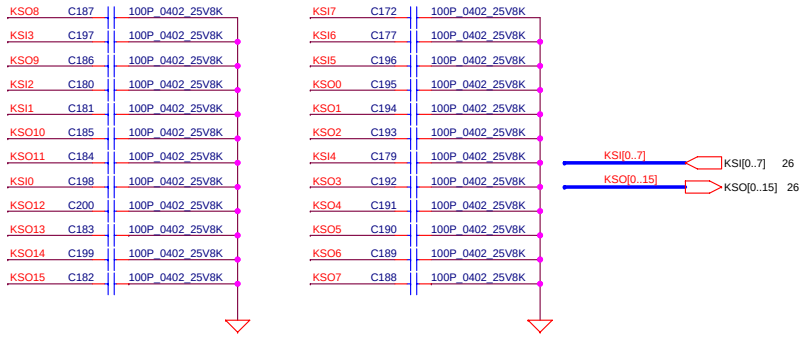
MICROPHONE IN JACK



HEADPHONE OUT JACK



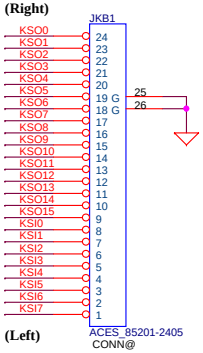




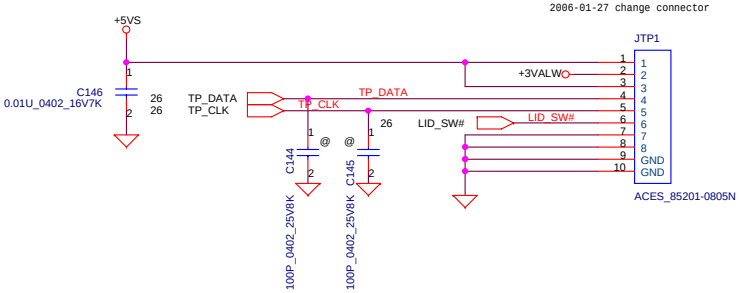
INT_KBD CONN.

TO M/B

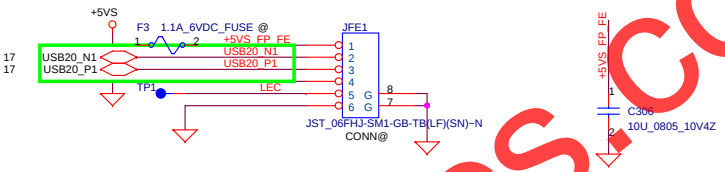
(Right)



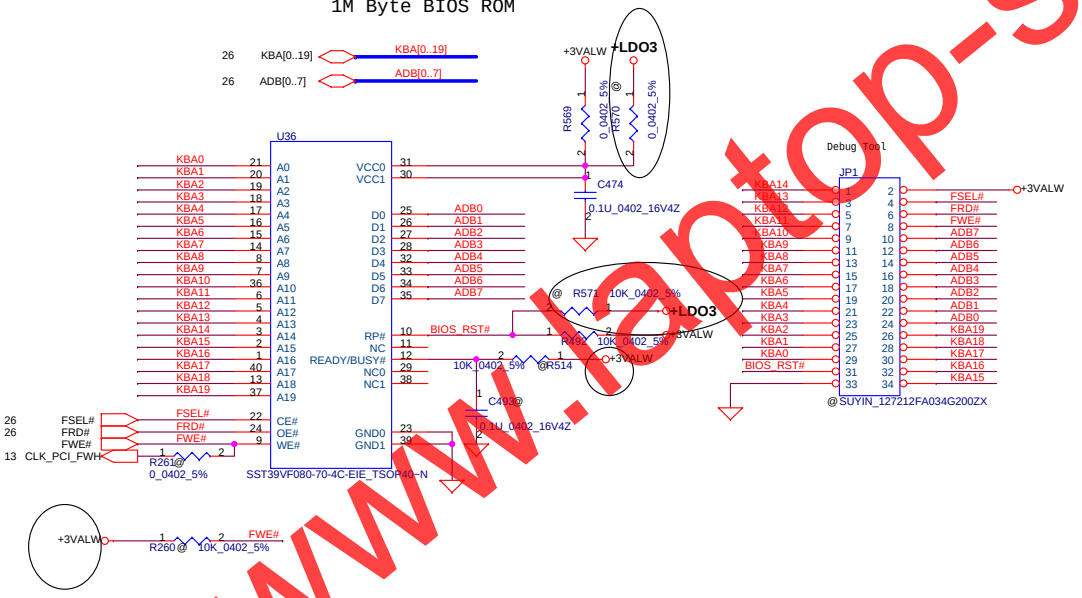
(Left)

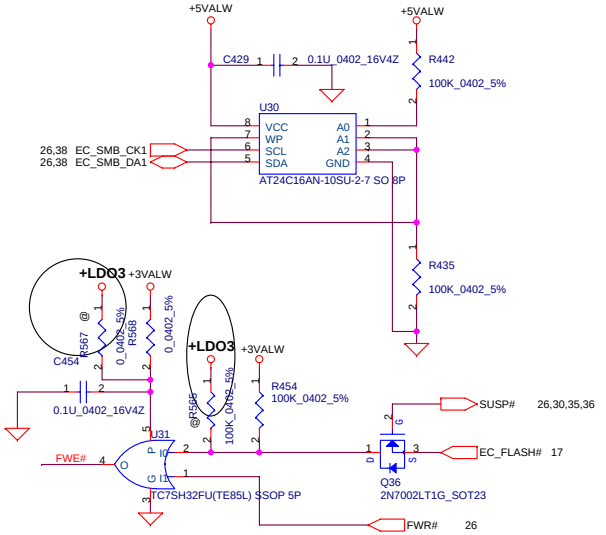


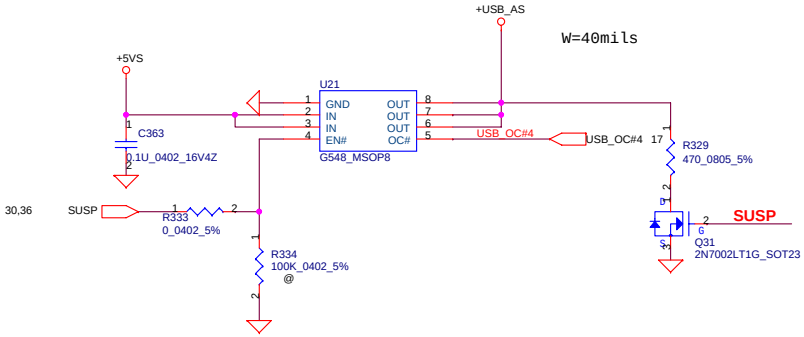
Felica Conn

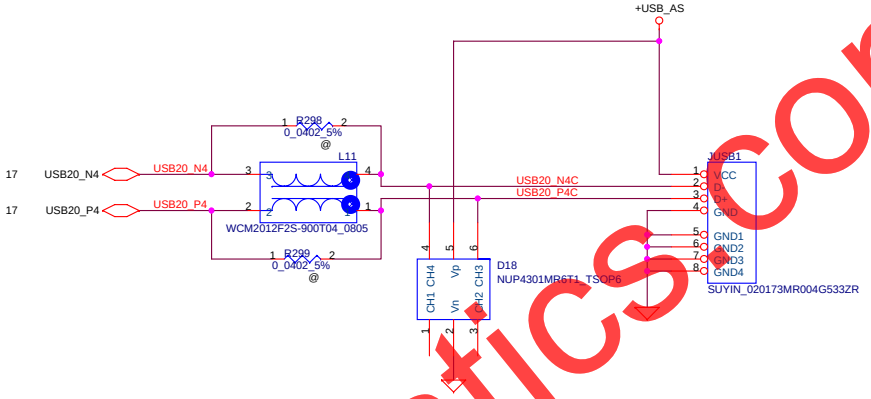


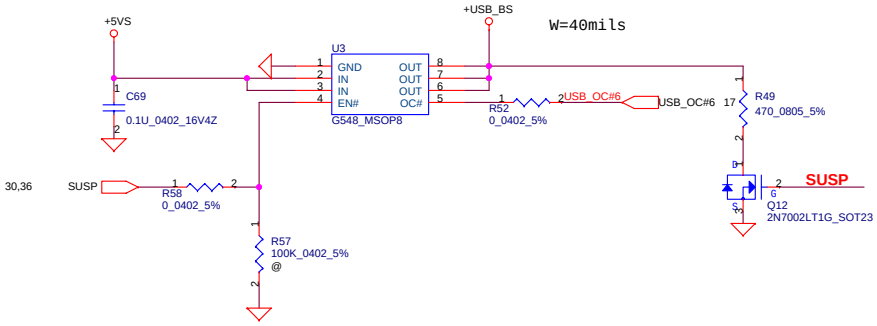
1M Byte BIOS ROM

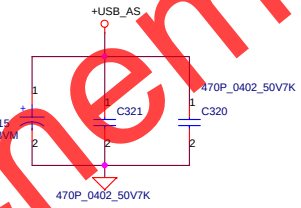


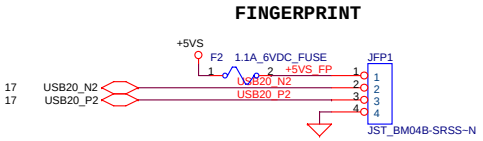


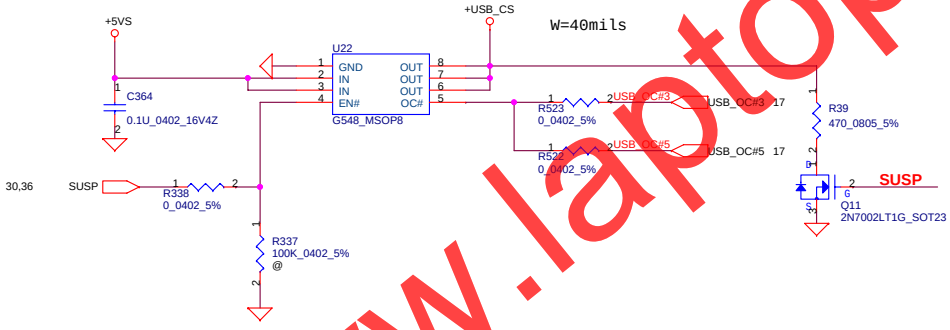


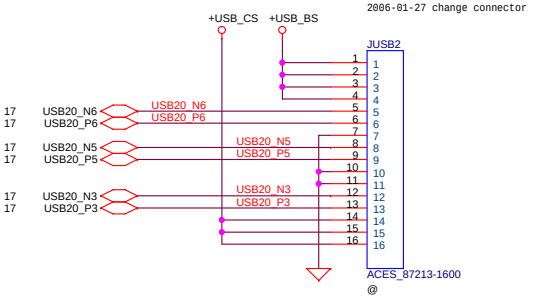


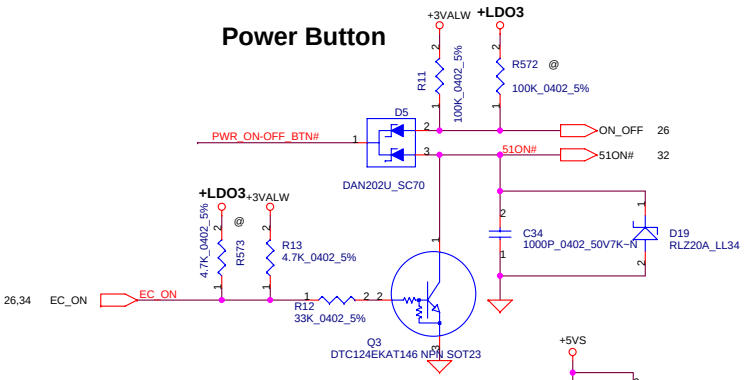




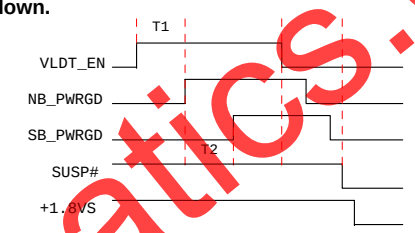


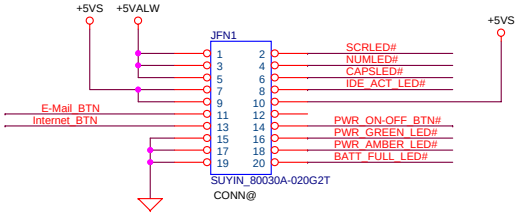


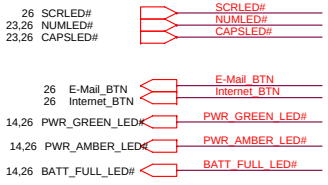


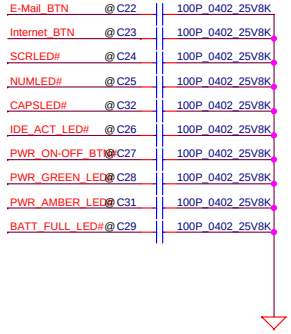


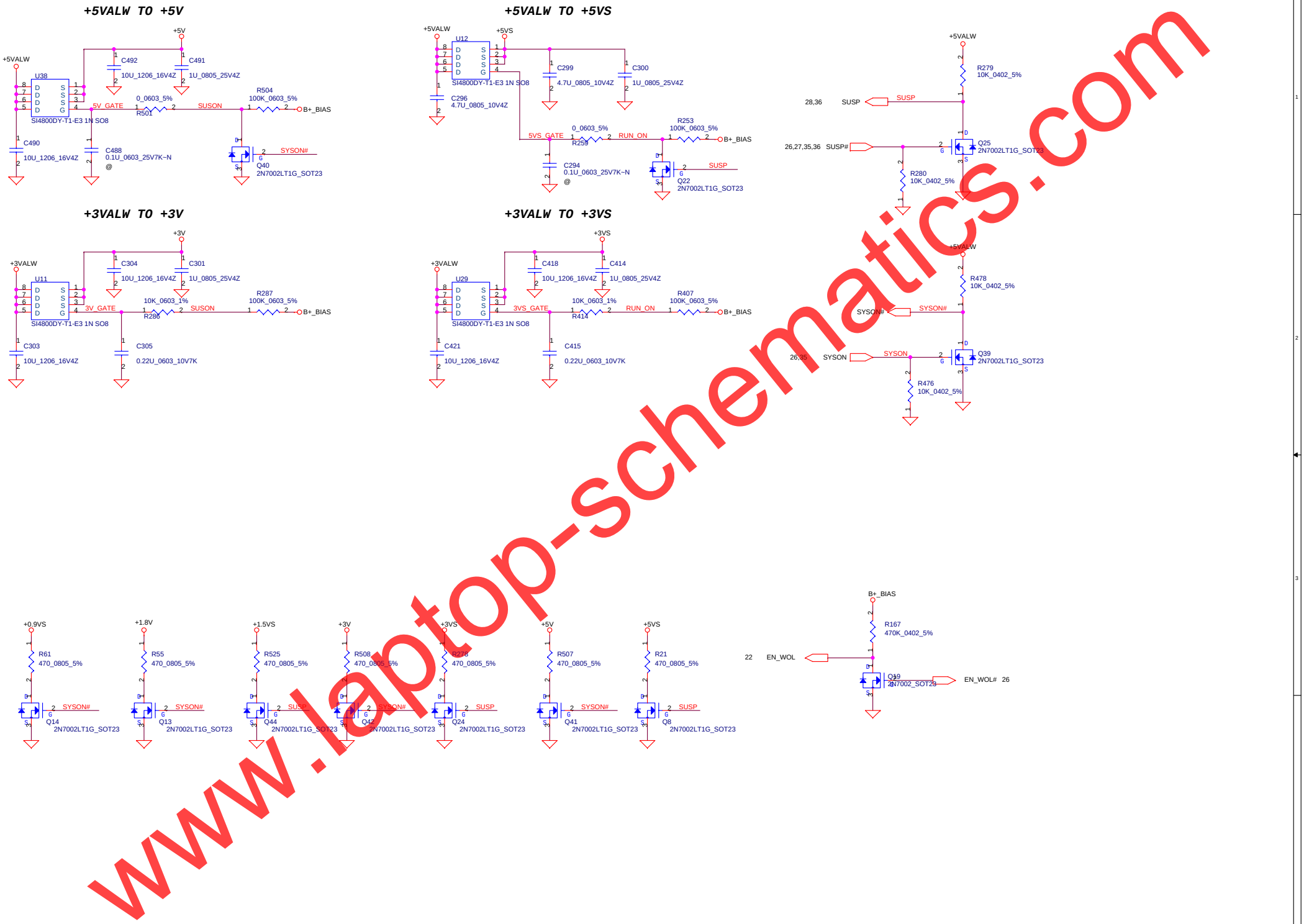
note:T1 minimum 15ms,T2 minimum 33ms/maximum 500ms, SUSP# goes to low after SB_PWRGD goes to low for power down.

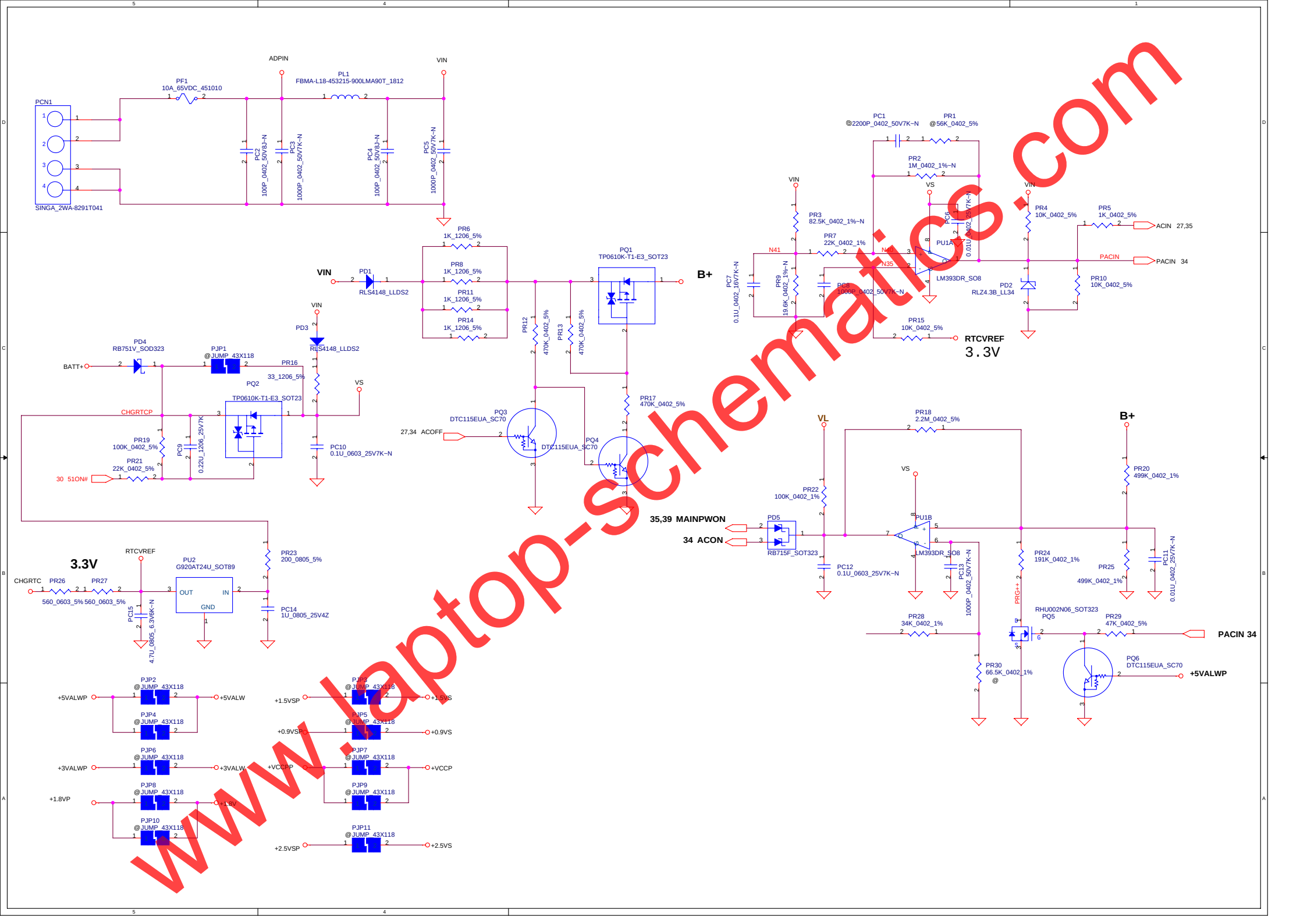


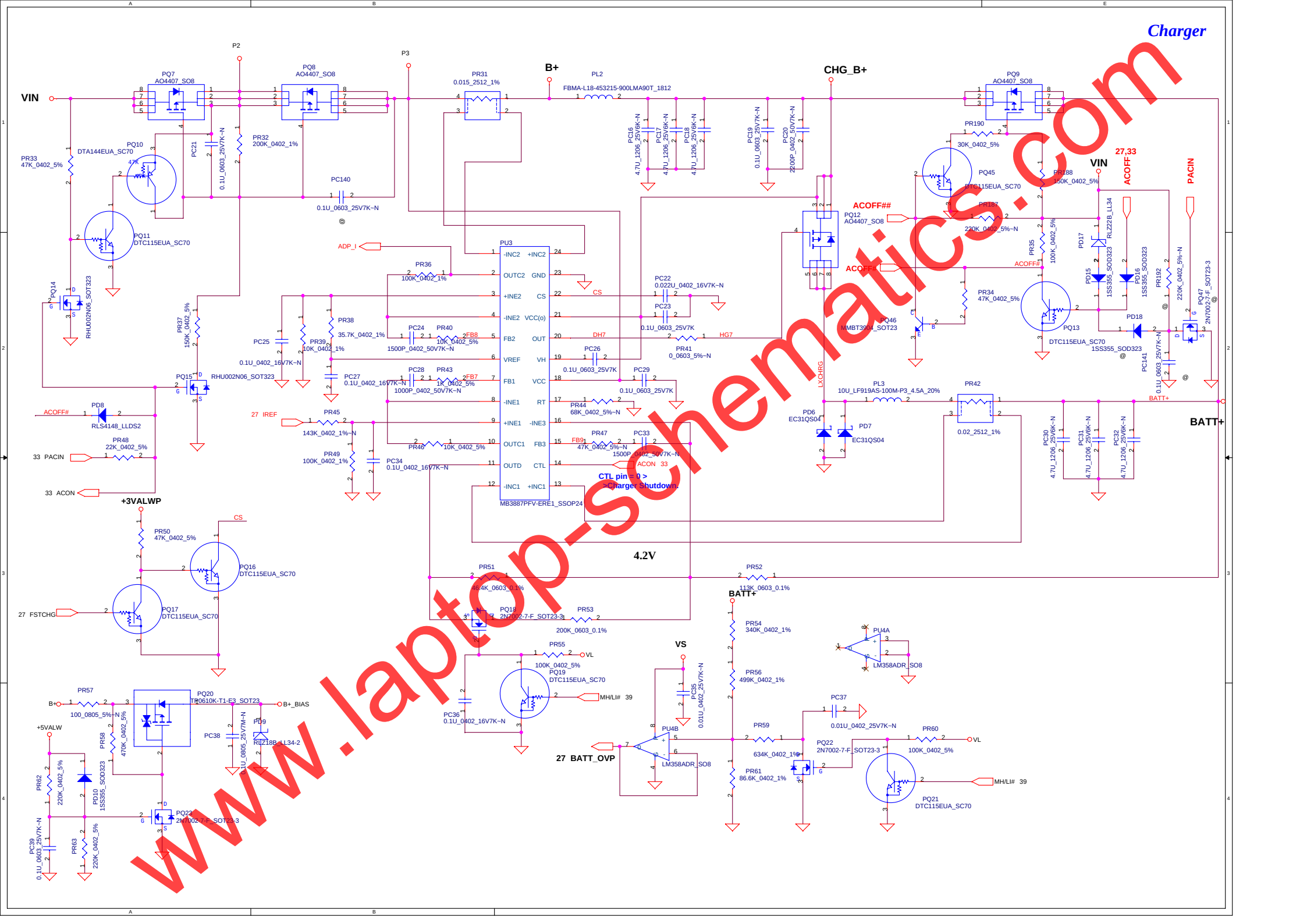




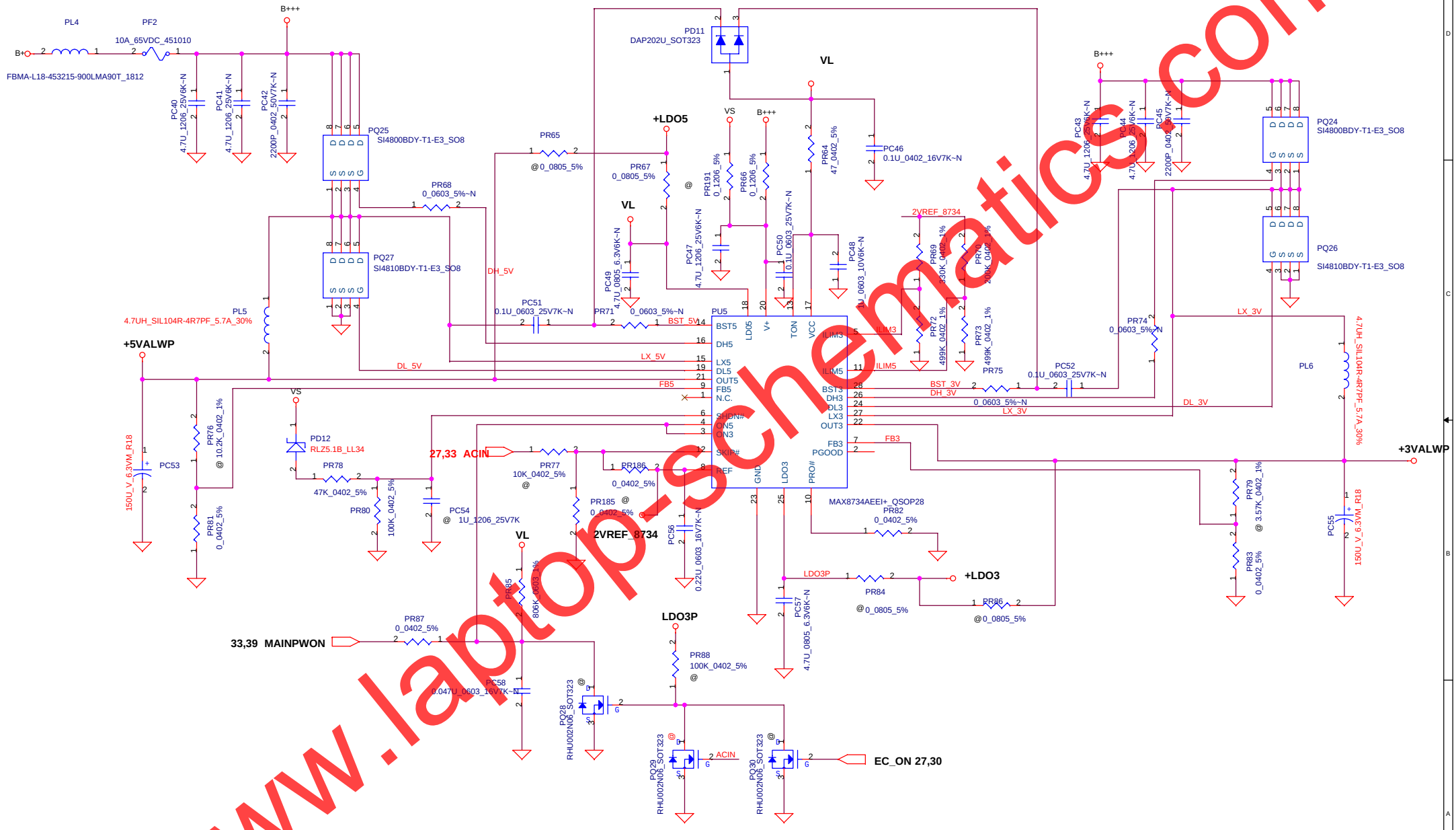


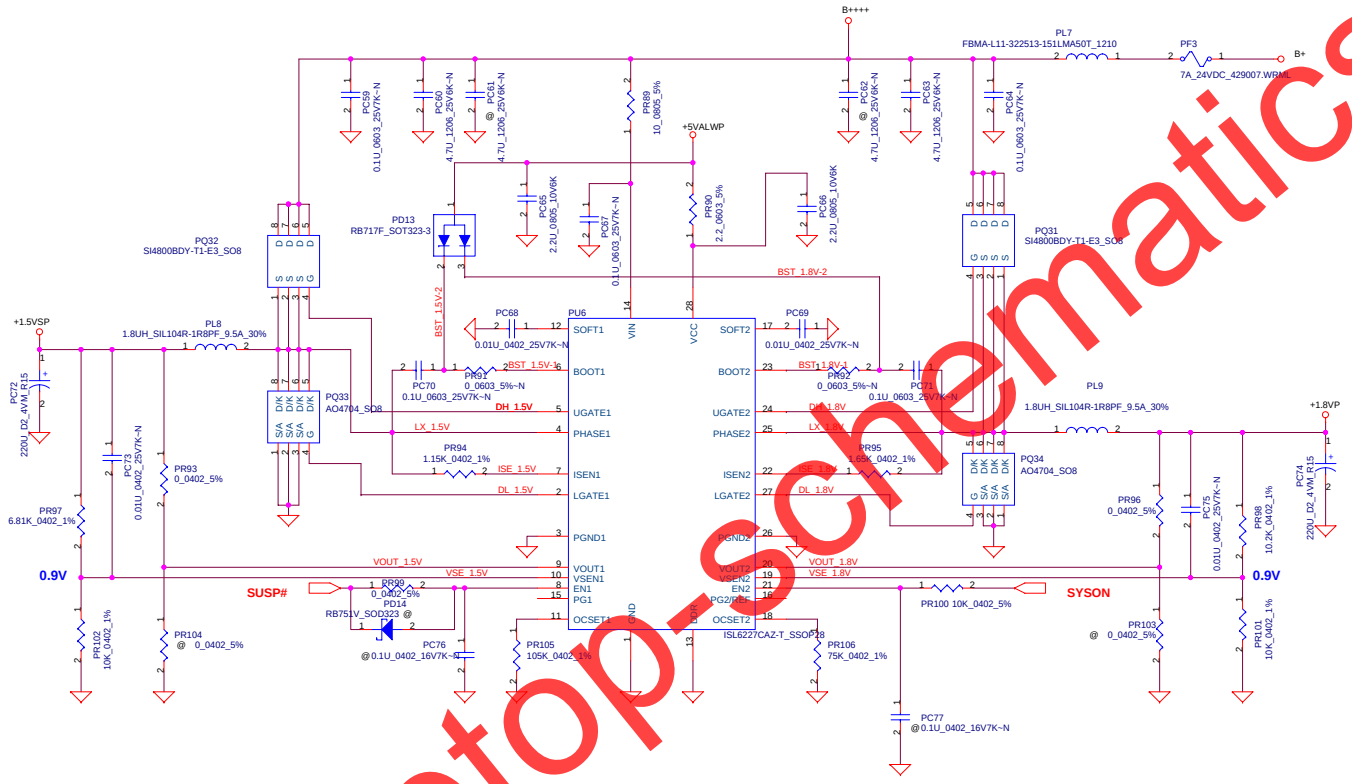


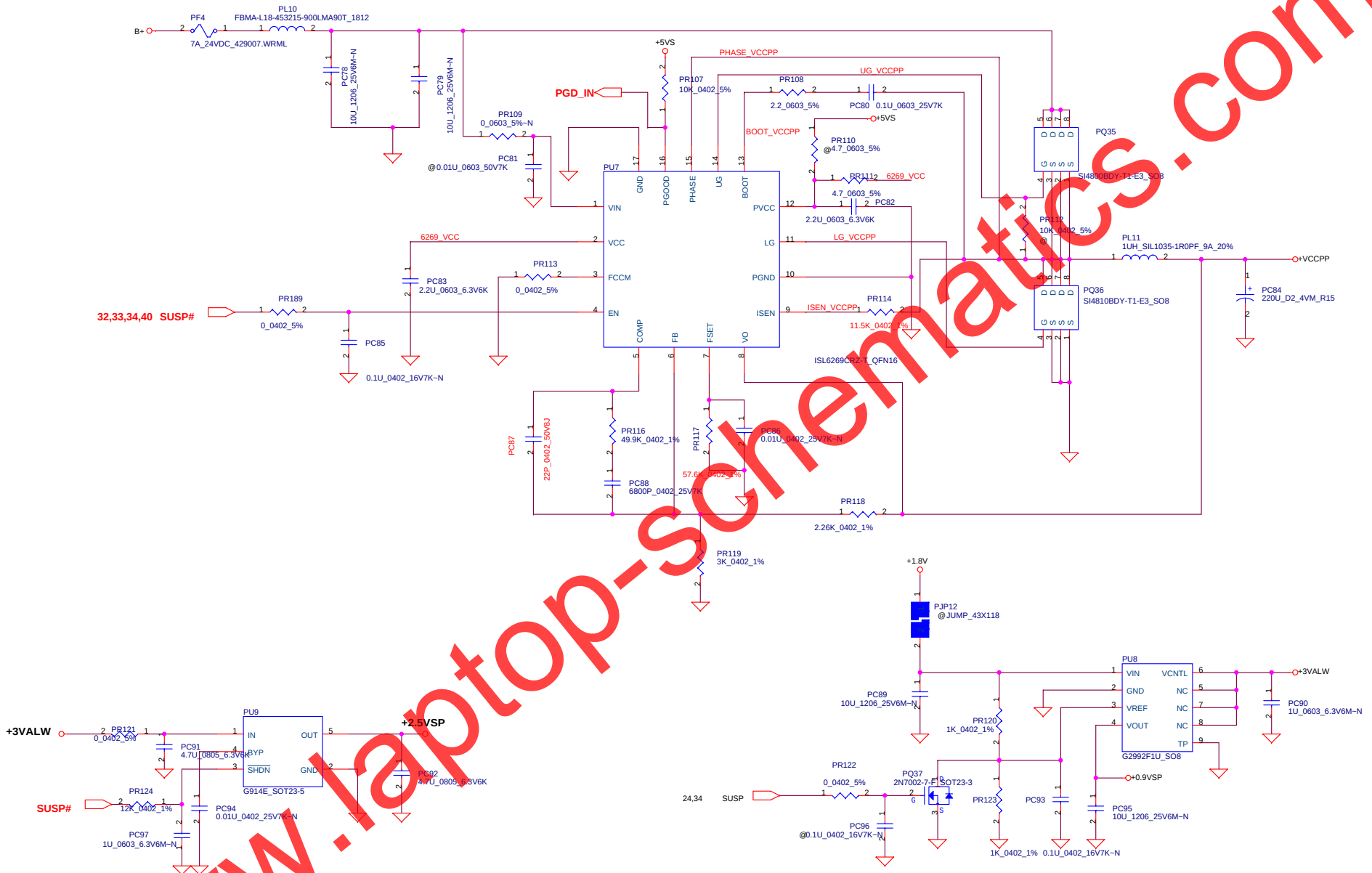


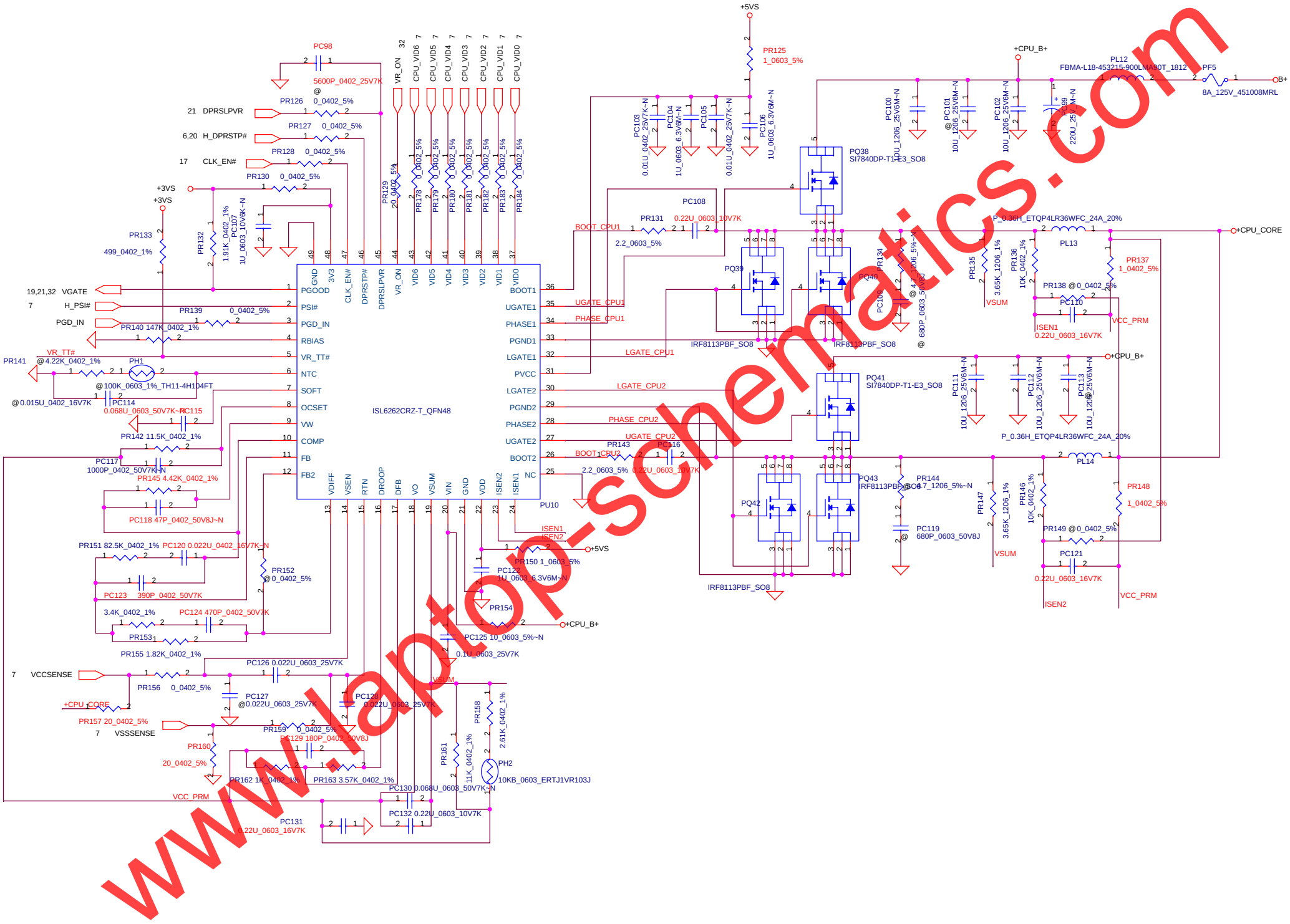


+3.3VALWP/+5VALWP









Battery Connect/OTP