


***LCFC Confidential***

***GE420 IO BOARD***

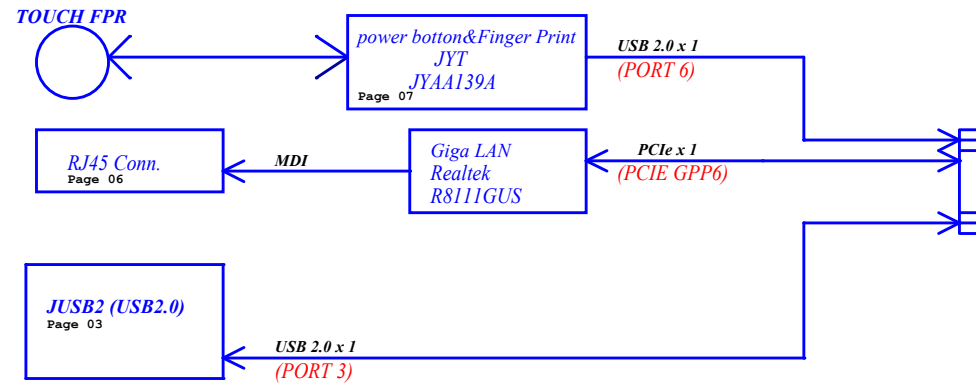
***NS- Rev0.1 Schematic***

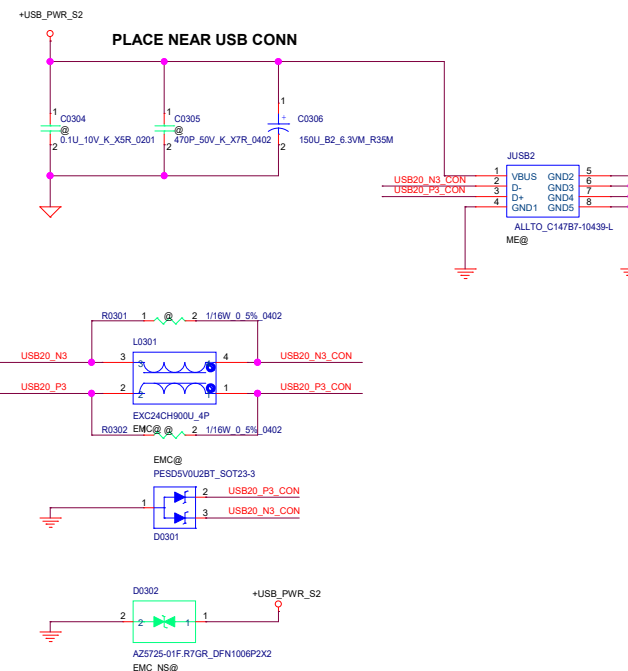
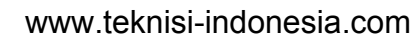
***AMD Renoir FP6 Processor with DDR4***



***2019-07-01 Rev0.1***

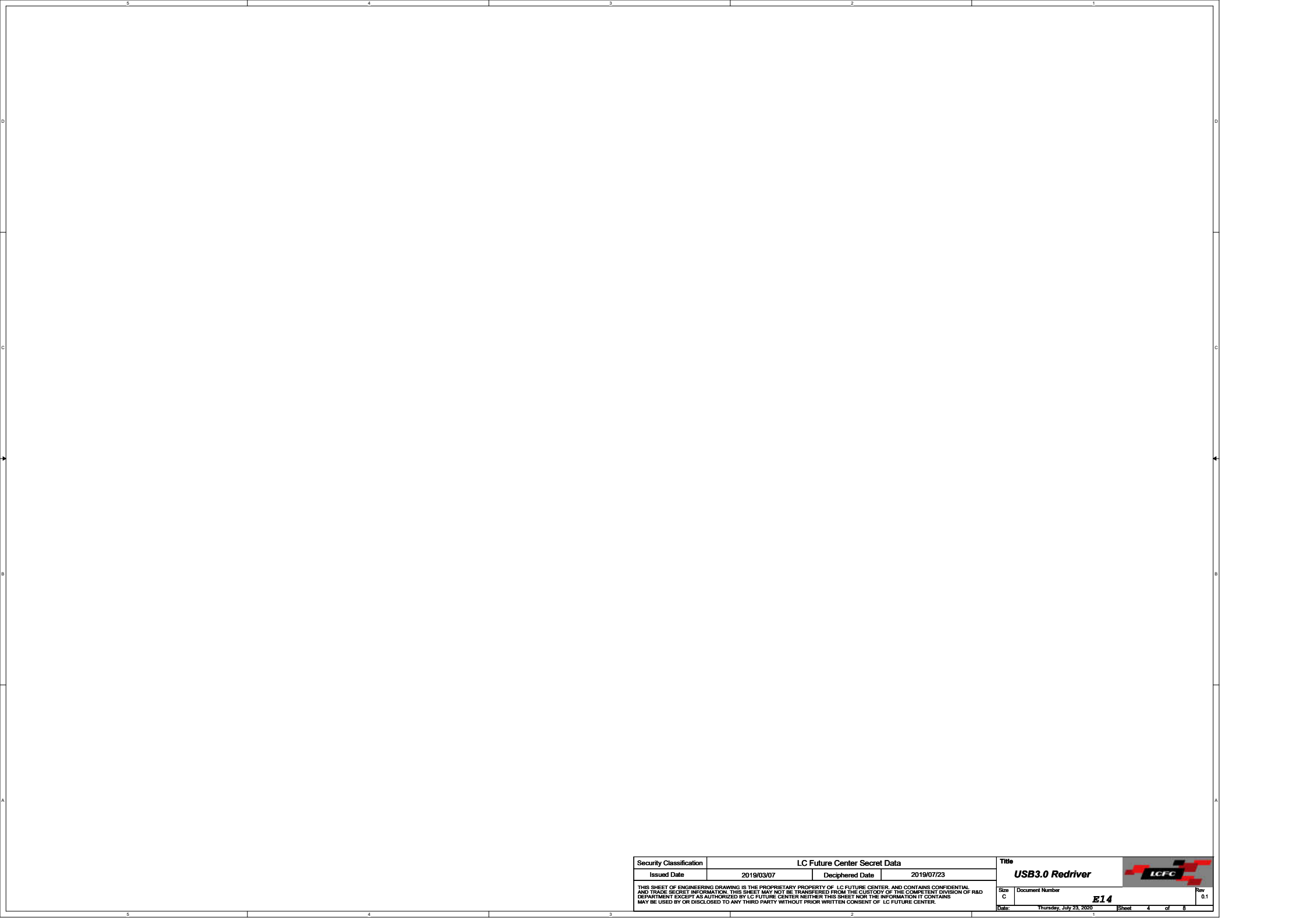
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Issued Date	20189/03/07	Deciphered Date	2019/07/23	Size	Document Number	Rev	
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# IO\_Board

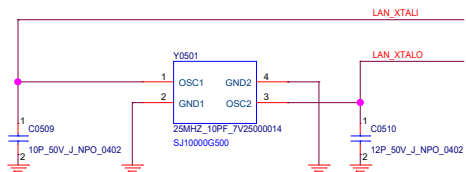
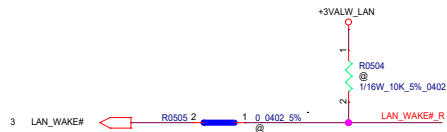
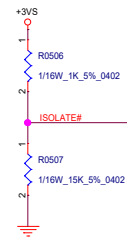
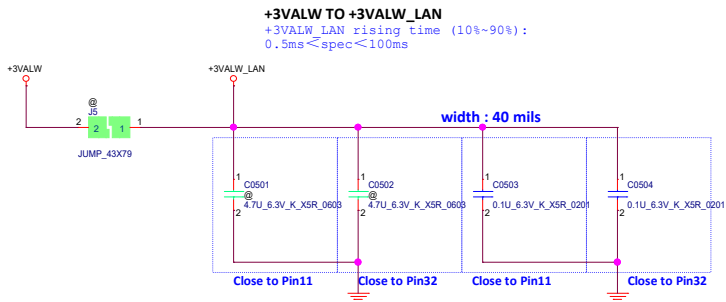




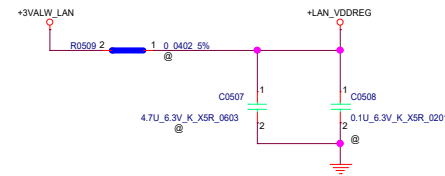
Security Classification		LC Future Center Secret Data		Title			
Issued Date	2019/03/07	Deciphered Date	2019/07/23	JOB CONN & USB3.0			
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				E14			
				Date: Thursday, July 23, 2020		Sheet 3 of 8	



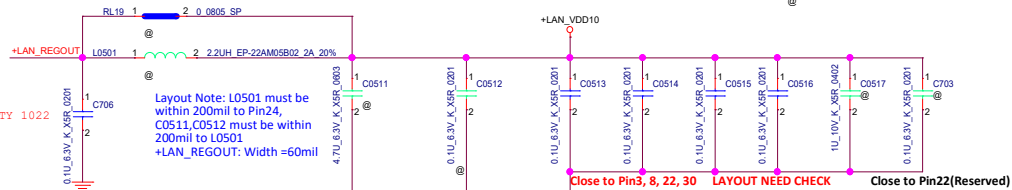
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Date: Thursday, July 23, 2020		Sheet		4	of 8



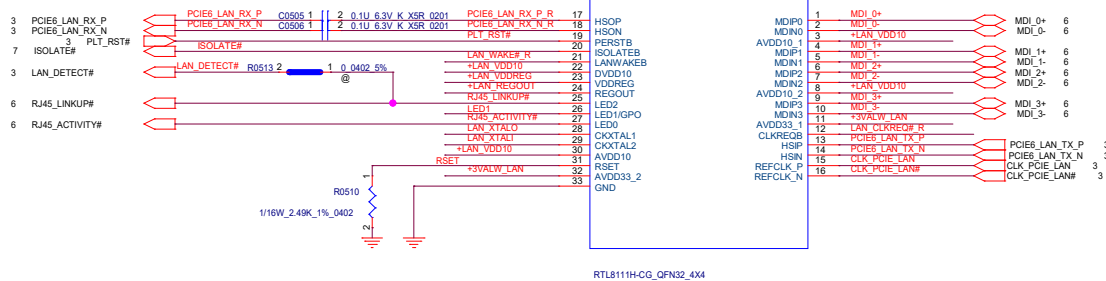
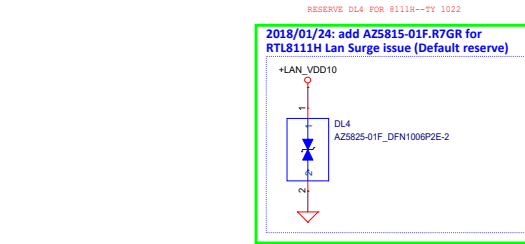
C0511/C0507/C0508 change BOM structure to RTL8111GUS--TY 1022



ADD C706 for 8111H--TY 1022



Layout Note: L0501 must be within 200mil to Pin24, C0511, C0512 must be within 200mil to L0501  
+LAN\_REGOUT: Width = 60mil



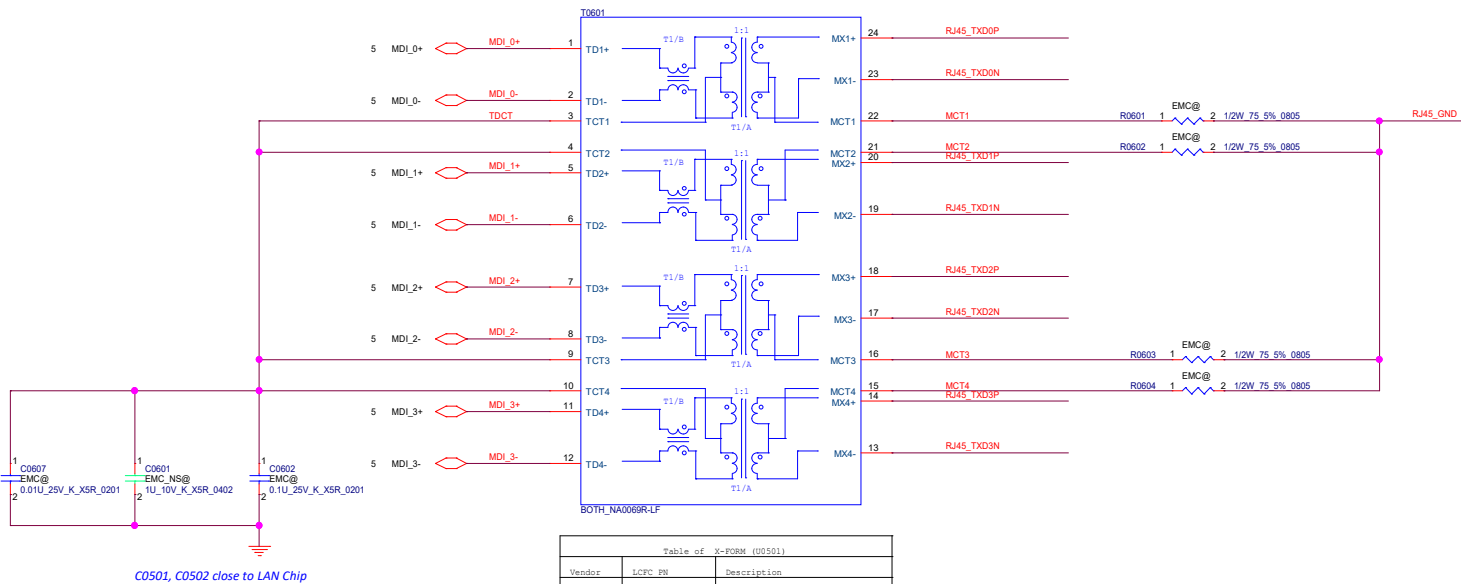
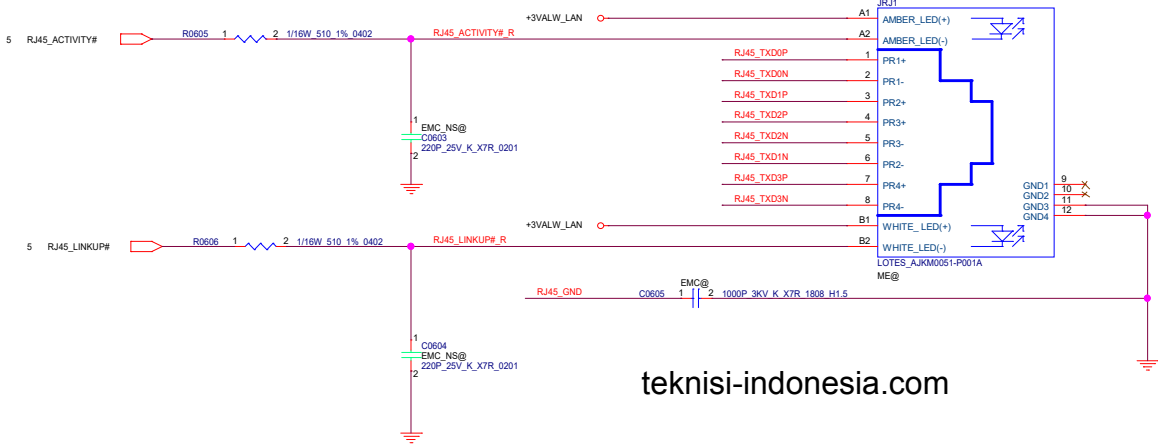
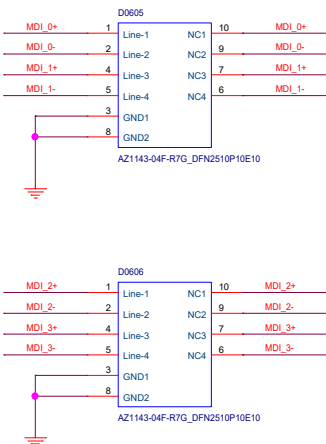


Table of X-FORM (U0501)		
Vendor	LCFC PN	Description
BOTHAND	8P050008800	8 X'FORM NA0069R LF LAN

### RJ-45 Conn.

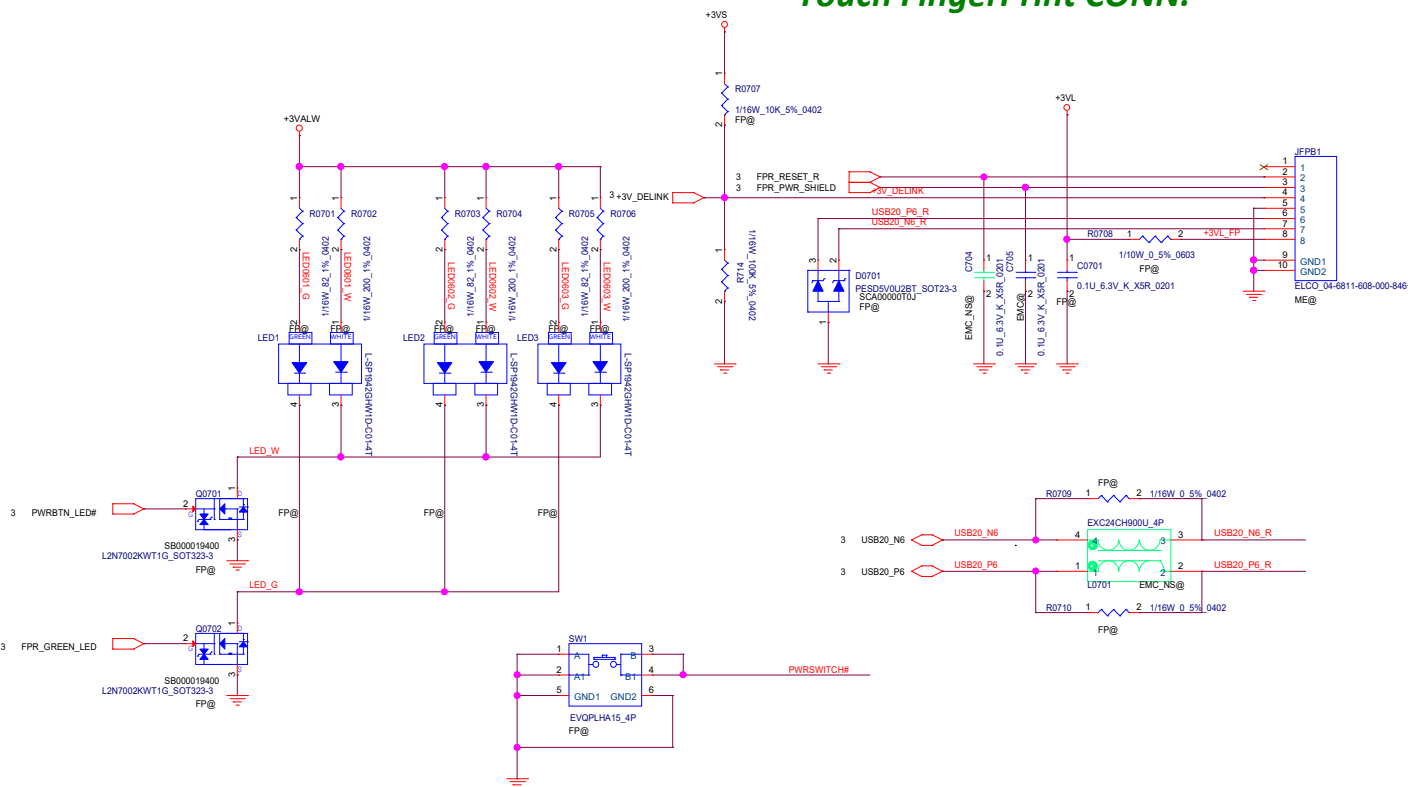


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# Touch FingerPrint CONN.

## Pin Assignment

Pin No.	Symbol	Description
1	LED	LED SIGNAL
2	RESETN	MCU RESETN SIGNAL
3	GPIO_AL0	POWER SHIELD
4	DELINK	POWER STATUS
5	DGND	GROUND
6	DP	USB DATA+
7	DM	USB DATA-
8	D3V3	POWER 3.3V



## PWRBTN

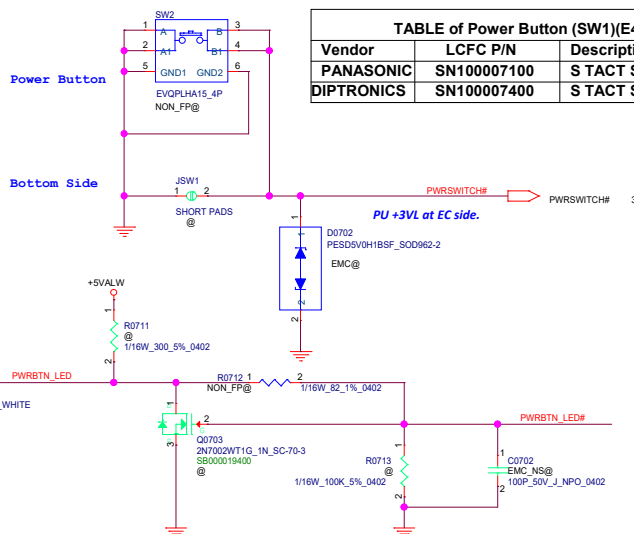


TABLE of Power Button (SW1)(E4A0)		
Vendor	LCFC P/N	Description
PANASONIC	SN100007100	S TACT SW EVQPLHA15 SPST PANAS H1.5 4P
DIPTRONICS	SN100007400	S TACT SW TJG-533-V-T/R SPST H1.5 6P

